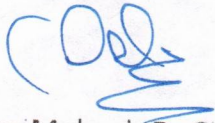
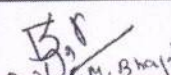
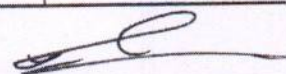


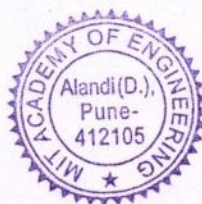
**2.3.4: Preparation and adherence of Academic Calendar and Teaching plans
by the institution**



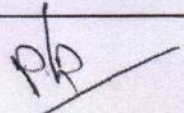
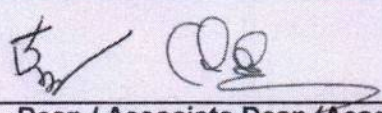
Dr. Mahesh D. Goudar
Director,
MITAoE, Alandi, Pune.

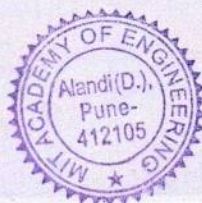


MIT Academy of Engineering		INSTITUTIONAL ACADEMIC CALENDER	
AN AUTONOMOUS INSTITUTE Alandi (D), Pune - 412 105		ACADEMIC YEAR	: 2017-2018
Class	SYBTech	SEM	: I
		w.e.f.	: 03 07 2017
Description		Start Date	End Date
Admission to SY BTech		26 06 2017	1 07 2017
Last date for Course adjustment		1 07 2017	1 07 2017
Cycle I			
Commencement of Cycle		3 07 2017	16 08 2017
Course Feedback I		17 07 2017	
In course Examination		24 07 2017	
Course Feedback II		7 08 2017	
End course Examination of Cycle		14 08 2017	16 08 2017
Showing the evaluated Answer sheet		18 08 2017	
Submission of grades by Faculty		21 08 2017	
Technical Student Activity		18 08 2017	19 08 2017
Cycle II			
Commencement of Cycle		21 08 2017	4 10 2017
Course Feedback I		4 09 2017	
In course Examination		11 09 2017	
Course Feedback II		25 09 2017	
End course Examination of Cycle		3 10 2017	4 10 2017
Showing the evaluated Answer sheet		6 10 2017	
Submission of grades by Faculty		10 10 2017	
Cultural Programme		16 09 2017	
Cycle III			
Commencement of Cycle		9 10 2017	28 11 2017
Course Feedback I		30 10 2017	
In course Examination		6 11 2017	
Course Feedback II		20 11 2017	
End course Examination of Cycle		27 11 2017	28 11 2017
Showing the evaluated Answer sheet		30 11 2017	
Submission of grades by Faculty		2 12 2017	
Re Examination		4 12 2017	8 12 2017
Diwali Holidays		14 10 2017	22 10 2017
Winter Holidays		29 11 2017	17 12 2017
 Coordinator: Dean Academics		 Director	

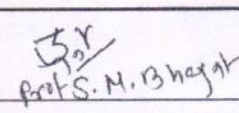
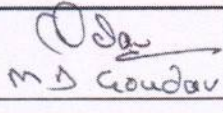
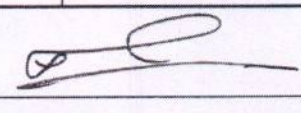


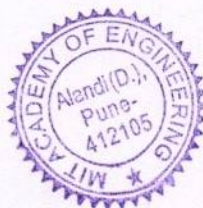
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MIT Academy of Engineering (An Autonomous Institute)	INSTITUTIONAL ACADEMIC CALENDAR	
Alandi (D), Pune - 412 105	ACADEMIC YEAR	2017 - 2018
	TERM / TRIMESTER	IV
	W.E.F.	2/1/2018
Description	Start Date	End Date
Cycle I		
Commencement of Cycle	02/01/2018	16/03/2018
Course Feedback	6/02/2018	---
In course Examination	05,12/02/2018	---
Annual Social Gathering	12/02/2018	17/02/2018
Showing the evaluated Answer sheet (ICE)	16/02/2018	---
Robocon 2018	01/03/2018	3/03/2018
End course Examination of Cycle	12/03/2018	14/03/2018
In Sem Examination of Semester Long Course	16/03/2018	---
Showing the evaluated Answer sheet	19/03/2018	23/03/2018
Cycle II		
Commencement of Cycle	19/03/2018	31/05/201
Course Feedback	24/04/2018	---
In course Examination	23,30/04/2018	---
Showing the evaluated Answer sheet (ICE)	4/05/2018	---
End Sem Examination of Semester Long Course	24/05/2018	---
End course Examination of Cycle	28/05/2018	31/05/2018
Showing the evaluated Answer sheet	05/06/2018	---
Registration for Re-Examination	6/6/2018	7/6/2018
Re-Examination	11/06/2018	16/06/2018
Summer Intership	1/06/2018	31/07/2018
Commencement of Next Semester	1/08/2018	---
 Time Table Coordinator	 Dean / Associate Dean (Academics)	



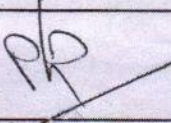
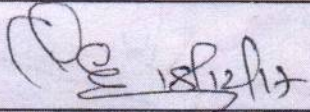
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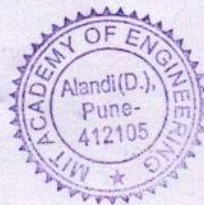
MIT Academy of Engineering AN AUTONOMOUS INSTITUTE Alandi (D), Pune - 412 105		INSTITUTIONAL ACADEMIC CALENDER	
		ACADEMIC YEAR	: 2017-2018
Class	Third Year & Final Year	SEM	: I
		w.e.f.	: 12 06 2017
Description		Start Date	End Date
Admissions of Third & Final Year		8 06 2017	10 06 2017
Commencement of Teaching		12 06 2017	18 10 2017
Feedback I by Student for Course Teacher		17 07 2017	
Feedback II by Student for Course Teacher		30 08 2017	
In-sem Examination		Not declared till date by SPPU	
Practical /Oral/Project exam		24 10 2017	9 11 2017
Theory Examination		14 11 2017	11 12 2017
Student Activity		18 08 2017	19 08 2017
 Prof. S. M. Bhargava		 M. D. Goudar	
Coordinator: Dean Academics		 Director	



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MIT Academy of Engineering (An Autonomous Institute)		INSTITUTIONAL ACADEMIC CALENDAR	
Alandi (D), Pune - 412 105		ACADEMIC YEAR	2017 - 2018
		TERM / TRIMESTER	II (TE, BE)
		W.E.F.	18/12/2017
Description		Start Date	End Date
Commencement of Semester		18/12/2017	6/04/2018
Annual Social Gathering		12/02/2018	17/02/2018
Robocon 2018		01/03/2018	3/03/2018

	
Time Table Coordinator	Dean / Associate Dean (Academics)



MIT Academy of Engineering AN AUTONOMOUS INSTITUTE Alandi (D), Pune - 412 105 F.Y. B.TECH				DEPARTMENT ACADEMIC CALENDER					
				ACADEMIC YEAR	:	2017-2018			
				SEMESTER	:	I			
				w.e.f.	:	01/08/2017			
MONTH - AUG-17	ACTIVITY	MONTH - SEP-17	ACTIVITY	MONTH - OCT-17	ACTIVITY	MONTH - NOV-17	ACTIVITY	MONTH - DEC-17	ACTIVITY
1	TERM START	1	Defaulter List Display	1		1		1	
2		2		2	Gandhi Jayanti	2	Defaulter List Display	2	ESE--M1
3		3		3	Defaulter List Display	3		3	
4		4	ISE1--M1	4	MSE PR EXAM CP/EGR in regular batch	4		4	ESE--EGR/CP
5		5	Anant Chaturdashi	5		5		5	ESE--EEE/AM
6		6		6		6		6	
7		7		7		7		7	
8		8	Student Feedback-1	8		8		8	ESE--PHY/CHEM
9		9		9	ISE2--M1	9		9	ESE--LC
10		10		10		10	Student Feedback-2	10	
11		11	ISE1--EEE/AM	11		11		11	
12		12		12		12		12	Paper Showing-ESE
13		13		13		13		13	Paper Showing-ESE
14		14		14		14		14	Re-Exam Registration
15	Independence Day	15		15		15		15	
16		16		16	Diwali Holidays	16		16	DT/ETT Project Exam
17	Exam Reg. begins	17		17		17	Provisional Detension List	17	
18		18	MSE EGR/CP/ LC	18		18		18	
19		19		19		19		19	
20		20		20		20		20	
21		21		21		21		21	Re-Exam--M1
22		22		22		22		22	
23		23		23		23		23	Re-Exam--PHY/CHEM
24	Exam Reg. Ends	24		24		24	Final Detension List	24	
25	Ganesh Chaturthi	25	MSE PHY/ CHEM	25		25		25	Christmas
26		26		26		26		26	Re-Exam--EEE/AM
27		27		27		27		27	
28		28		28		28		28	Re-Exam--EGR/CP/ LC
29		29	Parents Meet	29		29	TERM END	29	Paper Showing-Re-Exam
30		30		30	ISE2--EEE/AM	30		30	
31				31				31	

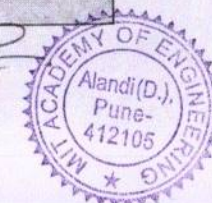
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FY Btech Coordinator

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TIME TABLE COORDINATOR

[Signature]
DIRECTOR



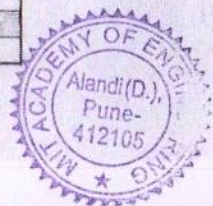
MIT Academy of Engineering AN AUTONOMOUS INSTITUTE Alandi (D), Pune - 412 105						DEPARTMENT ACADEMIC CALENDER					
DEPARTMENT OF F Y B Tech						ACADEMIC YEAR	:	2017-2018			
						SEM	:	II			
						w.e.f.	:	02/01/2018			
MONTH JAN	ACTIVITY	MONTH FEB	ACTIVITY	MONTH MARCH	ACTIVITY	MONTH APRIL	ACTIVITY	MONTH MAY	ACTIVITY	MONTH JUNE	ACTIVITY
1		1		1		1		1		1	Summer Term Reg
2	Semester Start	2		2		2	ISE2 EEE/AM	2	ESE PHY/CHEM	2	
3		3		3		3		3		3	
4		4		4		4		4	ESE EEE/AM	4	
5		5		5	MSE PHY/CHEM	5		5	ESE LC	5	
6		6	ISE 1 M-II	6		6		6		6	summer Term
7		7		7		7		7	ESE EGR / CP	7	
8		8		8		8		8		8	
9		9		9		9		9		9	
10		10		10		10		10	Paper Showing	10	
11		11		11		11		11	Paper Showing	11	
12		12	ISE 1 EEE/AM	12	MSE CPEGR/Pre Exam	12		12	OT/ETT Proj. Exam	12	
13		13		13		13		13		13	summer Term
14		14	Annual Social Gathering	14		14		14	Result	14	
15		15		15		15		15		15	
16		16		16	Parents Meet	16		16	Re Exam Reg.	16	
17		17		17		17		17	Re Exam Reg.	17	
18		18		18		18		18		18	
19		19	Shiv Jayanti	19		19		19	Re Exam EGR / CP	19	
20		20		20		20		20		20	summer Term
21		21		21		21		21	Re Exam M-II	21	
22		22		22		22		22		22	
23		23		23		23		23	Re Exam EEE / AM	23	
24		24		24		24		24		24	
25		25		25		25		25	Re Exam PHY / CHEM	25	
26	Republic Day	26	MSE EGR/CP/LC	26	ISE2 M-II	26		26	Re Exam LC	26	Summer Term Exam
27		27		27		27	Term End	27		27	
28		28		28		28		28	Paper Showing	28	
29				29		29		29	Paper Showing	29	
30				30		30	ESE M-II	30		30	
31				31				31	Summer Term Reg		

TIME TABLE INCHARGE

HEAD OF DEPARTMENT

TIME TABLE COORDINATOR

Dean Academic/Associate Dean Academic



MIT Academy of Engineering (An Autonomous Institute) Alandi (D), Pune - 412 105	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2018 - 2019
	TERM	VII & VIII (BE)
	W.E.F.	15/06/2018

Description	Start Date	End Date
Semester - VII, 2018 - 2019		
Teaching	15 / 06 / 2018	17 / 10 / 2018
Practical / Oral / Project Examination	20 / 10 / 2018	03 / 11 / 2018
Theory Examination	14 / 11 / 2018	08 / 12 / 2018
Semester - VIII, 2018 -2019		
Teaching	17 / 12 / 2018	09 / 04 / 2019
Practical / Oral / Project Examination	11 / 04 / 2019	25 / 04 / 2019
Theory Examination	02 / 05 / 2019	27 / 05 / 2019



15.8
Dean - Academics

PA

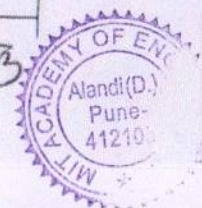
MIT Academy of Engineering AN AUTONOMOUS INSTITUTE Alandi (D), Pune - 412 105				DEPARTMENT ACADEMIC CALENDER					
F.Y. B.TECH				ACADEMIC YEAR		:	2018-2019		
				SEMESTER		:	I		
				w.e.f.		:	01/08/2018		
MONTH - AUG-18	ACTIVITY	MONTH - SEP-18	ACTIVITY	MONTH - OCT-18	ACTIVITY	MONTH - NOV-18	ACTIVITY	MONTH - DEC-18	ACTIVITY
1	TERM START	1		1	MSE EEE/AM	1		1	
2		2		2	Gandhi Jayanti	2	Defaulter List Display	2	
3		3	Defaulter List Display	3		3		3	ESE Maths-I
4		4		4		4		4	
5		5		5		5		5	ESE PHY/CHEM
6		6		6		6		6	
7		7		7		7	DIWALI VACATION	7	ESE EGR/CP
8		8		8	MSE EGR/CP Prac	8		8	
9		9		9		9		9	
10		10		10		10		10	ESE EEE/AM
11		11		11		11		11	ESE LC
12		12		12		12		12	
13		13	Ganesh Chaturthi	13		13		13	
14		14		14		14		14	Paper Showing
15	Independence Day	15		15	MSE Maths-I	15		15	
16		16		16		16		16	DT/ETT Project
17		17		17		17		17	
18		18		18	Dashhera	18		18	Result
19		19		19	MSE EGR/CP	19		19	
20		20		20		20	Provisional Detension List	20	Reexam Reg
21		21		21		21		21	
22	Bakri ID	22		22		22		22	
23		23		23		23		23	
24		24	MSE PHY/CHEM	24		24		24	Reexam ESE EGR/CP/LC
25		25		25		25		25	Christmas
26		26		26	Parents Meet	26	Final Detension List	26	Reexam ESE EEE/AM
27		27		27		27		27	
28		28		28		28		28	Reexam ESE Phy/Chem
29		29		29		29		29	
30		30		30		30	TERM END	30	
31				31				31	Reexam ESE M-I

TIME TABLE INCHARGE

FY BTECH COORDINATOR

TIME TABLE COORDINATOR

DIRECTOR

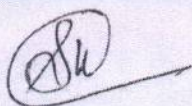


MIT Academy of Engineering (An Autonomous Institute) Alandi (D), Pune - 412 105	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2018 - 2019
	TERM	II (FY BTECH)
	W.E.F.	7/01/2019

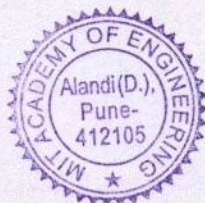
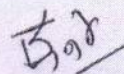
Description	Start Date	End Date
Semester - II (FY BTECH)		
Commencement and Conclusion	7th Jan 2019	4th May 2019
Annual Social Gathering	11th - 17th Feb. 2019	
In Course Examination	11th March 2019 (Monday)	
	18th March 2019 (Monday)	
	25th March 2019 (Monday)	
	1st April 2019 (Monday)	
Technical Event	22nd - 23rd March 2019	
Final Detention List (FDL)	19th April 2019	
Term End (Practical Exams in last week)	3rd May 2019	
Preparatory Leave	4th - 21st May 2019	
End Course Examination	22nd May 2019 (Wednesday)	
	23rd May 2019 (Thursday)	
	24th May 2019 (Friday)	
	25th May 2019 (Saturday)	
	27th May 2019 (Monday)	
Paper Showing	31st May 2019	
Result Declaration	4th June 2019	
Registration for Re - Examination	5th - 6th June 2018	
Re - Examination	10th - 13th June 2018	



Description	Start Date	End Date
Result Declaration(Re - Examination)	17th June 2019	
Summer School (Registration)	18th - 19th June 2019	
Summer School	20th June 2019 - 13th July 2019	
Summer School(Examination)	15th July 2019 (Monday)	
	17th July 2019(Wednesday)	
	19th July 2019(Friday)	
	20th July 2019(Saturday)	
Internship	1st June 2019 - 12th July 2019	
Orientation	15th - 31st July 2019	
Admission to Second Year and Verification from Student Section	upto 30th July 2019	
Course Registration for Second Year	31st July - 2nd Aug 2019	
Commencement of Next Academic Year	5th August 2019	



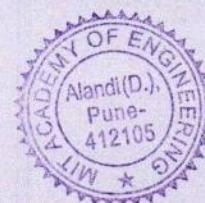
Timetable Co-Ordinator

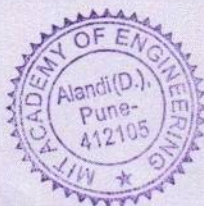
Dean - Academics

MIT Academy of Engineering (An Autonomous Institute) Alandi (D), Pune - 412 105	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2018 -2019
	TERM / TRIMESTER	III (SY) & V (TY)
	W.E.F.	1/8/2018

Description	Start Date	End Date
Semester - III & V		
Commencement and Conclusion	1st Aug. 2018	29th Dec. 2018
Cycle - I, Semester - III & V, 2018 - 2019		
Cycle - I	1st Aug. 2018	12th Oct. 2018
Course Feedback	3rd Sept. 2018	7th Sept. 2018
In course Examination	3rd Sept. 2018 - SY (Monday)	
	4th Sept. 2018 - TY (Tuesday)	
	10th Sept. 2018 - SY (Monday)	
	11th Sept. 2018 - TY (Tuesday)	
Final Detention List (FDL)	27th Sept. 2018	
In Sem Examination of Semester Long Course	6th Oct. 2018 - SY (Saturday)	
	8th Oct 2018 - TY (Monday)	
End Course Examination of Cycle Long Courses	9th Oct. 2018 - SY (Tuesday)	
	10th Oct. 2018 - TY (Wednesday)	
	11th Oct. 2018 - SY (Thursday)	
	12th Oct. 2018 - TY (Friday)	
Re-Exam Cycle - I	22nd Oct. 2018 - SY (Monday)	
	23rd Oct 2018 - TY (Tuesday)	
	29nd Oct. 2018 - SY (Monday)	
	30th Oct. 2018 - TY (Tuesday)	



Description	Start Date	End Date
Cycle - II, Semester - III & V, 2018 - 2019		
Cycle - II	15th Oct. 2018	29th Dec. 2018
Diwali Vacation	3rd Nov. 2018	11th Nov. 2018
Course Feedback	12th Nov. 2018	16th Nov. 2018
In course Examination	19th Nov. 2018 - SY (Monday)	
	21st Nov. 2018 - TY (Wednesday)	
	26th Nov. 2018 - SY (Monday)	
	27th Nov. 2018 - TY (Tuesday)	
Final Detention List (FDL)	14th Dec. 2018	
End Sem Examination of Semester Long Course	22nd Dec. 2018 - SY (Saturday)	
	24th Dec. 2018 - TY (Monday)	
End Course Examination of Cycle Long Courses	26th Dec. 2018 - SY (Wednesday)	
	27th Dec. 2018 - TY (Thursday)	
	28th Dec. 2018 - SY (Friday)	
	29th Dec. 2018 - TY (Saturday)	
Commencement of Semester - IVth and VIth	2nd January 2019	
Re-Examination Cycle - II	7th Jan. 2019 - SY (Monday)	
	8th Jan 2019 - TY (Tuesday)	
	14th Jan 2019 - SY (Monday)	
	15th Jan. 2019 - TY (Tuesday)	
	21st Jan 2019 - SY (Monday)	
	22nd Jan 2019 - TY (Tuesday)	

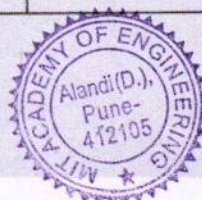


B.R.
Dean - Academics

PK

MIT Academy of Engineering (An Autonomous Institute) Alandi (D), Pune - 412 105	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2018 - 2019
	TERM / TRIMESTER	IV (SY) & VI (TY)
	W.E.F.	7/01/2019

Description	Start Date	End Date
Semester - IV (SY BTECH) & VI (TY BTECH)		
Commencement and Conclusion	7th Jan 2019	4th May 2019
Cycle - I, Semester - IV & VI, 2018 - 2019		
Cycle - I	7th Jan. 2019	9th March 2019
In course Examination of Cycle Long Courses (Cycle - I)	4th Feb. 2019 - SY (Monday)	
	5th Feb. 2019 - TY (Tuesday)	
Annual Social Gathering	11th - 17th Feb. 2019	
Final Detention List (FDL - Cycle - I)	5th March 2019	
End Course Examination of Cycle Long Courses (Cycle - I)	11th March 2019 - SY (Monday)	
	12th March 2019 - TY (Tuesday)	
Cycle - II, Semester - IV & VI, 2018 - 2019		
Cycle - II	13th March 2019	4th May 2019
In Course Examination of Semester Long Courses (Cycle - II)	18th March 2019 - SY (Monday)	
	19th March 2019 - TY (Tuesday)	
	25th March 2019 - SY (Monday)	
	26th March 2019 - TY (Tuesday)	
	1st April 2019 - SY (Monday)	
	2nd April 2019 - TY (Tuesday)	
Technical Event	22nd - 23rd March 2019	
In course Examination of Cycle Long Courses (Cycle - II)	8th April 2019 - SY (Monday)	
	9th April 2019 - TY (Tuesday)	



Description	Start Date	End Date
Final Detention List (FDL) Cycle Long (Cycle - II) and Semester Long Courses	30th April 2019	
Term End (Cycle - II) including Practical Exams	4th May 2019	
Preparatory Leave	5th - 12th May 2019	
End Course Examination of Cycle Long Courses (Cycle - II)	13th May 2019 - SY (Monday)	
	14th May 2019 - TY (Tuesday)	
End Course Examination of Semester Long Courses	15th May 2019 - TY (Wednesday)	
	16th May 2019 - SY (Thursday)	
	17th May 2019 - TY (Friday)	
	18th May 2019 - SY (Saturday)	
	20th May 2019 - TY (Monday)	
	21st May 2019 - SY (Tuesday)	
Paper Showing	25th May 2019	
Result Declaration	31st May 2019	
Registration for Re - Examination	31st May - 1st June 2019	
Re - Examination	4th - 12th June 2019	
Internship	1st June - 31st July 2019	
Admission to next Class and Verification from Student Section	upto 30th July 2019	
Course Registration for Next Year	31st July - 2nd August 2019	
Commencement of Next Academic Year	5th August 2019	

P/O
12/11/18
Timetable Co-Ordinator

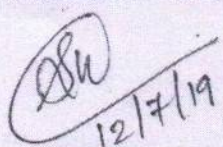


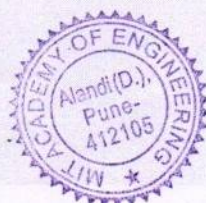
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12 Nov 2018
Dean - Academics
DEAN ACADEMICS
MIT Academy of Engineering
Alandi (D.), Pune-412 105.


MIT Academy of Engineering (An Autonomous Institute) Alandi (D), Pune - 412 105	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2019 - 2020
	TERM / TRIMESTER	I (FY,SY, TY & BTech)
	W.E.F.	1/08/2019

Description	Start Date	End Date
Semester - I (FY, SY, TY & B.TECH)		
Commencement and Conclusion	1st Aug 2019 (Thursday)	29th Nov 2019 (Friday)
FY B Tech Student Induction Program	1st Aug 2019 (Thursday)	
In Semester Examination	FY: 7th Oct 2019 (Mon) - 11th Oct 2019 (Fri)	
	TY: 23rd Sep 2019 (Mon)- 27th Sep 2019 (Fri)	
	SY & B Tech: 30th Sep 2019 (Mon) - 4th Oct 2019 (Fri)	
Diwali Holidays	26th Oct 2019 (Sat) - 3rd Nov 2019 (Sun)	
Term End including all Practical Exams	FY: 6th Dec 2019 (Fri)	
	SY, TY, BTech: 29th Nov 2019 (Fri)	
Preparatory Leave	FY: 7th Dec 2019 (Sat) - 11th Dec 2019 (Wed)	
	SY, TY, B Tech: 30th Nov 2019 (Sat) - 5th Dec 2019 (Thu)	
End Semester Examination	6th Dec 2019 (Fri) - 18th Dec 2019 (Wed)	
Fest Talk	20th Dec 2019 (Fri)- 21st Dec 2019 (Sat)	
Result Declaration	27th Dec 2019 (Fri)	
Registration for Re - Examination	30th Dec 2019 (Mon) - 31st Dec 2019 (Tue)	
Re - Examination	1st Jan 2020 (Wed) - 6th Jan 2020 (Mon)	
Commencement of semester II	7th Jan 2020 (Tue)	

Note: Practical exams for all the programs to be conducted in the last week of the Academic Term


12/7/19
Timetable Co-ordinator




Dean - Academics

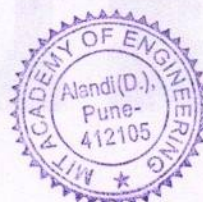
MIT Academy of Engineering An Autonomous Institute Affiliated to SPPU	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2019 - 2020
	TERM / TRIMESTER	II (FY, SY, TY and B Tech)
	W.E.F.	6/01/2020

Description	Start Date	End Date
Semester - II (FY, SY, TY and B.TECH)		
Commencement and Conclusion	6 Jan 2020 (Monday)	20 Jun 2020 (Saturday)
Technical Event	11 (Tuesday) and 12 (Wednesday) Feb 2020	
Annual Social Gathering	13 (Thursday) to 15 (Saturday) Feb 2020	
In Semester Examination	FY, SY, TY and BTech: 2 March (Monday) - 9 Mar 2020 (Monday)	
Project Reviews	First Review (TY, BTech) : 6 Feb (Thursday) 2020	
	Second Review (TY, BTech) : 13 Mar (Friday) 2020	
Teaching End and Final Detention List	23 April 2020 (Thursday)	
Practical Exams	FY, SY, TY, B Tech: 24 April (Friday) - 2 May 2020 (Saturday)	
Project Exams	2 May 2020 (Saturday)	
Term End	FY, SY, TY, BTech: 2 May 2020 (Saturday)	
Preparatory Leave	FY, SY, TY, B Tech: 3 May (Sunday) - 10 May 2020 (Sunday)	
End Semester Examination	FY, SY, TY, B Tech: 11 May (Monday) - 21 May 2020 (Thursday)	
Result Declaration	1 June (Monday)	
Registration for Re - Examination	FY, SY, TY, B Tech: 2 June (Tuesday) - 3 June 2020 (Wednesday)	
Re - Examination	FY, SY, TY, B Tech: 8 June (Monday) - 17 June 2020 (Wednesday)	
Result Declaration (Re Examination)	20 June 2020 (Saturday)	
Remedial Term Registration	22 June 2020 (Monday) - 23 June 2020 (Tuesday)	
Remedial Term	24 June (Wednesday) - 10 July 2020 (Friday)	
Remedial Term Examination	13 July (Monday) - 17 July 2020 (Friday)	
Internship	22 May (Friday) to 18 July (Saturday) 2020	
Orientation for the next Academic Year	13 July (Monday) - 18 July 2020 (Saturday)	
Commencement of Next Academic Year	20 July 2020 (Monday)	

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Dean - Academics
21.11.2019

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21/11/19
Controller of Examinations

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Director
21.11.19.



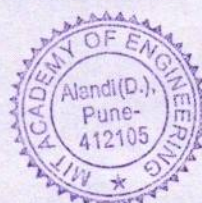
MIT Academy of Engineering An Autonomous Institute Affiliated to SPPU	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2020 - 2021
	TERM	I and II (FY B Tech)
	W.E.F.	11/01/2021

Description	Start Date	End Date
Semester - I and II (FY B Tech)		
Commencement and Conclusion (Sem I)	11 Jan 2021 (Monday)	30 Apr 2021 (Friday)
Preparatory Leave - MSE	23 Feb 2021 (Tuesday) - 24 Feb 2021 (Wednesday)	
Mid Semester Examination(Sem I)	25 Feb 2021 (Thursday) - 27 Feb 2021 (Saturday)	
Practical Exams(Sem I)	16 Apr 2021 (Friday) - 22 Apr 2021 (Thursday)	
Term End and Final Detention List(Sem I)	22 April 2021 (Thursday)	
Preparatory Leave - ESE	23 Apr 2021 (Friday) - 25 Apr 2021 (Sunday)	
End Semester Examination(Sem I)	26 Apr 2021 (Monday) - 30 Apr 2021 (Friday)	
Result Declaration-ESE (Sem I)	7 May 2021 (Friday)	
Commencement and Conclusion (Sem II)	3 May 2021 (Monday)	31 Aug 2021 (Tuesday)
Preparatory Leave - MSE	20 Jun 2021 (Sunday) - 21 Jun 2021 (Monday)	
Mid Semester Examination(Sem II)	22 Jun 2021 (Tuesday) - 24 Jun 2021 (Thursday)	
Practical Exams(Sem II)	2 Aug 2021 (Monday) - 7 Aug 2021 (Saturday)	
Term End and Final Detention List(Sem II)	7 Aug 2021 (Saturday)	
Preparatory Leave - ESE	8 Aug 2021(Sunday) - 10 Aug 2021 (Tuesday)	
End Semester Examination(Sem II)	11 Aug 2021 (Wednesday) - 16 Aug 2021 (Monday)	
Result Declaration-ESE (Sem II)	20 Aug 2021 (Friday)	
Registration for Re examination (Sem I & II)	21 Aug 2021 (Saturday) - 23 Aug 2021 (Monday)	
Re examination (Sem I & II)	24 Aug 2021 (Tuesday) - 31 Aug 2021 (Tuesday)	

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23/12/2020
Dean - Academics

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23/12/2020
Controller of Examinations

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23.12.2020
Director



MIT Academy of Engineering An Autonomous Institute Affiliated to SPPU	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2020 - 2021
	TERM / TRIMESTER	II (SY, TY and B Tech)
	W.E.F.	4/01/2021

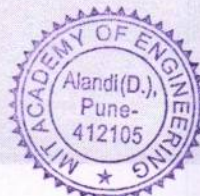
Description	Start Date	End Date
Semester - II (SY, TY and B.TECH)		
Commencement and Conclusion	4 Jan 2021 (Monday)	19 Jun 2021 (Saturday)
Mid Semester Examination	1 March (Monday) - 10 Mar (Wednesday) 2021	
Annual Social Gathering	14 Mar (Sunday) to 15 Mar (Monday) 2021	
Technical Event	16 Mar (Tuesday) and 17 Mar (Wednesday) 2021	
Project Reviews	First Review (SY, TY, BTech) : 11 Feb (Thursday) 2021	
	Second Review (SY, TY, BTech) : 25 Mar (Thursday) 2021	
	Final Review (SY, TY, BTech) : 2 May (Sunday) 2021	
SLIP Design- Review	21 Mar (Sunday) to 30 Mar (Tuesday) 2021	
Teaching End and Final Detention List	30 April (Friday) 2021	
Practical Exams	3 May (Monday) - 8 May (Saturday) 2021	
Term End	SY, TY, BTech: 8 May 2021 (Saturday)	
Preparatory Leave	9 May (Sunday) - 11 May 2021 (Tuesday)	
End Semester Examination	12 May (Wednesday) - 22 May (Saturday) 2021	
SLIP Implementation- Review	21 May (Friday) to 28 May (Friday) 2021	
Result Declaration	31 May (Monday) 2021	
Registration for Re - Examination	2 June (Wednesday) - 3 June (Thursday) 2021	
Re - Examination	7 June (Monday) - 16 June (Wednesday) 2021	
Result Declaration (Re Examination)	19 June (Saturday) 2021	
Remedial Term Registration	20 June 2021 (Sunday) - 21 June 2021 (Monday)	
Remedial Term	22 June (Tuesday) - 17 July 2021 (Saturday)	
Remedial Term Examination	19 July (Monday) - 23 July 2021 (Friday)	
Internship	23 May (Sunday) to 25 July (Sunday) 2021	
Commencement of Next Academic Year	2 Aug (Monday) 2021	

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Dean - Academics

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23/12/2020
Controller of Examinations

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Director

23-12-2020



MIT Academy of Engineering An Autonomous Institute Affiliated to SPPU Alandi (D), Pune - 412 105	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2021 - 2022
	TERM / TRIMESTER	I (Second Year)
	W.E.F.	9/9/2021

Description	Start Date	End Date
Semester - I (Second Year)		
Commencement and Conclusion	09-09-2021 (Thursday)	14-01-2022 (Friday)
Mid Semester Examination	8 Nov (Monday) - 13 Nov 2021 (Saturday)	
Project Reviews	First Review:- 29 Oct. (Friday) 2021	
	Second Review:- 17 Dec (Friday) 2021	
Teaching End and Final Detention List	7 Jan 2022 (Friday)	
Practical Exams	8 Jan (Saturday) - 14 Jan 2022 (Friday)	
Project Exams	14 Jan 2021 (Friday)	
Term End	14 Jan 2022 (Friday)	
Preparatory Leave	15 and 16 Jan (Sunday) 2022	
End Semester Examination	17 Jan (Monday) - 24 Jan 2022 (Monday)	
Result Declaration	31 Jan (Monday) 2022	
Registration for Re - Examination	1 Feb (Tuesday) - 3 Feb 2022 (Thursday)	
Re - Examination	7 Feb (Monday) - 12 Feb 2022 (Saturday)	
Result Declaration (Re Examination)	20 Feb (Sunday) 2022	
Commencement of Next Semester	1 Feb (Tuesday) 2022	

Baglekar
30/5/21

Prepared and Proposed By

Mr. Nilesh C Baglekar

**Institutional Time table
coordinator**

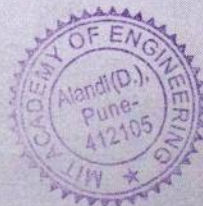
Recommended By

Dr. Sunita Barve and Dr. Arika Kotha

Dean Academics and COE

Approved by

Dr. Mahesh D. Goudar

Director

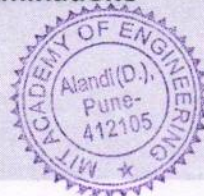
MIT Academy of Engineering An Autonomous Institute Affiliated to SPPU	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2021 - 2022
	TERM / TRIMESTER	I (TY and B Tech)
	W.E.F.	02/08/2021

Description	Start Date	End Date
Semester - I (TY and B.TECH)		
Commencement and Conclusion	2 Aug 2021 (Monday)	27 Nov 2021 (Saturday)
Mid Semester Examination	TY and B.Tech: 20 Sept 2021 (Monday) - 26 Sept 2021 (Sunday)	
Project Reviews	First Review: 16 Sept 2021 (Thursday)	
	Second Review: 21 Oct 2021 (Thursday)	
Teaching End and Final Detention List	27 Nov 2021 (Saturday)	
Practical Exams	TY, B Tech: 29 Nov (Monday) - 9 Dec 2021 (Thursday)	
Project Exams	10 Dec 2021 (Friday)	
Term End	TY, B.Tech: 10 Dec 2021 (Friday)	
Preparatory Leave	TY, B Tech: 11 Dec 2021 (Saturday) - 13 Dec 2021 (Monday)	
End Semester Examination	TY, B Tech: 14 Dec (Tuesday) - 24 Dec 2021 (Friday)	
Result Declaration	2 Jan 2022 (Sunday)	
Registration for Re - Examination	TY, B Tech: 3 Jan 2022 (Monday) - 5 Jan 2022 (Wednesday)	
Re - Examination	TY, B Tech: 11 Jan 2022 (Tuesday) - 20 Jan 2022 (Thursday)	
Result Declaration (Re Examination)	25 Jan 2022 (Tuesday)	
Orientation for the Next Semester	27 Dec 2021 (Monday)	
Commencement of Next Academic Year	3 Jan 2022 (Monday)	

S. B. Chavre
18.06.2021
Dean - Academics

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18/6/2021
Controller of Examinations

[Signature]
Director



MIT

Academy of Engineering
(An Autonomous Institute)

SCHOOL ACADEMIC CALENDER

Alandi (D), Pune - 412 105	ACADEMIC YEAR	:	2021-2022
SCHOOL OF HUMANITIES AND ENGINEERING SCIENCES	SEM	:	I & II
	W.E.F	:	22/12/2021

DEC	ACTIVITY	JAN	ACTIVITY	FEB	ACTIVITY	MAR	ACTIVITY	APR	ACTIVITY	MAY	ACTIVITY	JUN	ACTIVITY	JUL	ACTIVITY	AUG	ACTIVITY
1		1	Induction Program	1		1	Shivratni	1		1	Sunday	1		1		1	Sem II- ESE
2		2	Sunday	2		2		2	Goodi Padwa	2		2		2		2	Sem II- ESE
3		3	Term I begins	3		3		3	Sunday	3	Ramzan Eid	3		3	Sunday	3	Sem II- ESE
4		4		4		4		4		4		4		4		4	Sem II- ESE
5	Sunday	5		5		5		5		5		5	Sunday	5		5	Sem II- ESE
6		6		6	Sunday	6	Sunday	6		6		6		6		6	
7		7		7		7		7		7		7		7		7	Sunday
8		8		8		8		8		8	Sunday	8		8		8	Moharram
9		9	Sunday	9		9		9	Sem I -Term End	9		9		9		9	
10		10		10		10		10	Sunday	10		10		10	Sunday	10	Result
11		11		11		11		11		11		11		11		11	
12	Sunday	12		12		12		12	PL	12		12	Sunday	12		12	
13		13		13	Sunday	13	Sunday	13		13		13	Sem II- MSE	13		13	
14		14		14		14		14	Ambedkar Jayanti	14		14	Sem II- MSE	14		14	Sunday
15		15		15		15		15	Sem I - ESE	15	Sunday	15	Sem II- MSE	15		15	Independence
16		16	Sunday	16		16		16	Sem I - ESE	16		16		16		16	Reexam
17		17		17		17		17	Sunday	17		17		17	Sunday	17	Reexam
18		18		18		18	Dhullivandan	18	Sem I - ESE	18		18		18		18	Reexam
19	Sunday	19		19	Chatrapati Shivaji Jayanti	19		19	Sem I - ESE	19		19	Sunday	19		19	Reexam
20		20		20	Sunday	20	Sunday	20	Sem I - ESE	20		20		20		20	Reexam
21		21		21	Sem I - MSE	21		21		21		21		21		21	Reexam
22		22		22	Sem I - MSE	22		22		22		22		22		22	Sunday
23	Orientation	23	Sunday	23	Sem I - MSE	23		23		23	Sunday	23		23		23	
24		24		24		24		24	Sunday	24		24		24		24	
25	CHRISTMAS	25		25		25		25	Term II begins	25		25		25		25	
26	Sunday	26	REPUBLIC DAY	26		26		26		26		26	Sunday	26		26	
27		27		27	Sunday	27	Sunday	27		27		27		27		27	
28		28		28		28		28		28		28		28		28	Sem II -Term End
29		29		29		29		29		29	Sunday	29		29		29	Sunday
30		30	Sunday	30		30		30		30		30		30		30	PL
31		31		31		31		31		31		31		31	Sunday	31	

Note: Dates for the above events in the Academic Calendar are subjected to changes. Every department has to

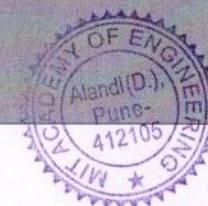
Note: Dates for the above events in the Academic Calendar are subjected to change. Exam timetable will be announced later.

Term I : Instructional days = 76

Term II : Instructional days = 77

TIME TABLE COORDINATOR

DEAN



MIT Academy of Engineering An Autonomous Institute Affiliated to SPPU	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2021 - 2022
	TERM / TRIMESTER	II (SY)
	W.E.F.	1/02/2022

Description	Start Date	End Date
Semester - II (S.Y.)		
Commencement and Conclusion	Tuesday, 1 February, 2022	Wednesday, 15 June, 2022
Mid Semester Examination	28 March (Monday) - 04 April 2022 (Monday)	
Project Reviews	First Review:- 11 March (Friday) 2022	
	Second Review:- 13 May (Friday) 2022	
Equilibrium 2022 (a national level Technical event)	Wednesday, 23 March and Thursday, 24 March 2022	
Annual Social Gathering	Friday, 25 March, and Saturday, 26 March 2022	
Teaching End and Final Detention List	Saturday, 21 May, 2022	
Practical Exams	23 May (Monday) - 4 June 2022 (Saturday)	
Project Exams	Saturday, 4 June 2022	
Term End	4 June 2022 (Saturday)	
Preparatory Leave	5 June (Sunday) - 7 June 2022 (Tuesday)	
End Semester Examination	8 June (Wednesday) - 15 June 2022 (Wednesday)	
Result Declaration	Friday, 24 June, 2022	
Registration for Re - Examination	25 June (Saturday) - 26 June 2022 (Sunday)	
Re - Examination	28 June (Tuesday) - 5 July 2022 (Tuesday)	
Re-Examination Result Declaration	Friday, 15 July, 2022	

Sports Days: 14 March to 22 March 2022 (After college Hours and on weekends)

[Signature]
14/12/21

Prepared and Proposed By
Mr. Nilesh C Baglekar

**Institutional Time table
coordinator**

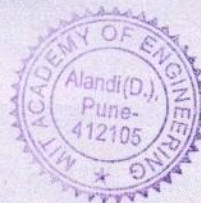
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14/12/21

Recommended By
Dr. Sunita Barve and Dr. Arika Kotha
**Deputy Director (Academics and
Research) and Controller of
Examination**

[Signature]
14/12/21

Approved by
Dr. Mahesh D Goudar

Director



MIT Academy of Engineering An Autonomous Institute Affiliated to SPPU	INSTITUTIONAL ACADEMIC CALENDAR	
	ACADEMIC YEAR	2021 - 2022
	TERM / TRIMESTER	II (TY and B Tech)
	W.E.F.	3/01/2022

Description	Start Date	End Date
Semester - II (TY and B.TECH)		
Commencement and Conclusion	Monday, 3 January, 2022	Saturday, 21 May, 2022
Mid Semester Examination	TY and BTech: 28 Feb (Monday) - 06 March 2022 (Sunday)	
Project Reviews	First Review:- TY: 11 Feb, B Tech: 18 Feb (Friday) 2022	
	Second Review:- TY: 8 April, B Tech: 15 April (Friday) 2022	
Equilibrium 2022 (a national level Technical event)	Wednesday, 23 March and Thursday, 24 March 2022	
Annual Social Gathering	Friday, 25 March, and Saturday, 26 March 2022	
Teaching End and Final Detention List	Saturday, 23 April, 2022	
Practical Exams	TY, B Tech: 25 April (Monday) - 7 May 2022 (Saturday)	
Project Exams	Saturday, 30 April and 7 May 2022	
Term End	TY, BTech: 7 May 2022 (Friday)	
Preparatory Leave	TY, B Tech: 8 May (Sunday) - 10 May 2022 (Tuesday)	
End Semester Examination	TY, B Tech: 11 May (Wednesday) - 21 May 2022 (Saturday)	
Result Declaration	6 June (Monday) 2022	
Registration for Re - Examination	TY, B Tech: 6 June (Monday) - 8 June 2022 (Wednesday)	
Re - Examination	TY, B Tech: 14 June (Tuesday) - 23 June 2022 (Thursday)	
Re-Examination Result Declaration	Thursday, 30 June, 2022	

Sports Days: 14 March to 22 March 2022 (After college Hours and on weekends)

N. C. Baglekar
14/12/21

Prepared and Proposed By
Mr. Nilesh C Baglekar

Institutional Time table
coordinator

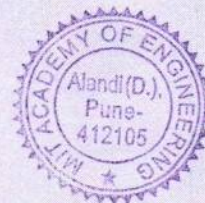
S. Barve
14/12/2021

Recommended By
Dr. Sunita Barve and Dr. Arika Kotha
Deputy Director (Academics and
Research) and Controller of
Examination

M. D. Goudar
14/12/21

Approved by
Dr. Mahesh D Goudar

Director



Chem

MITAcademy of
Engineering

(An Autonomous Institute Affiliated to Savitribai Phule Pune University)

INSTITUTIONAL ACADEMIC CALENDAR

Second Year B.Tech (Term I)

SEPTEMBER

S	M	T	W	T	F	S
				1	2	3
4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
25	26	27	28	29	30	

OCTOBER

S	M	T	W	T	F	S
						1
2	3	4	5	6	7	8
9	10	11	12	13	14	15
16	17	18	19	20	21	22
23	24	25	26	27	28	29
30	31					

NOVEMBER

S	M	T	W	T	F	S
		1	2	3	4	5
6	7	8	9	10	11	12
13	14	15	16	17	18	19
20	21	22	23	24	25	26
27	28	29	30			

DECEMBER

S	M	T	W	T	F	S
				1	2	3
4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
25	26	27	28	29	30	31

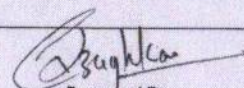
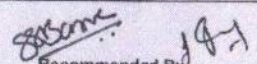
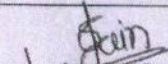
JANUARY

S	M	T	W	T	F	S
1	2	3	4	5	6	7
8	9	10	11	12	13	14
15	16	17	18	19	20	21
22	23	24	25	26	27	28
29	30	31				

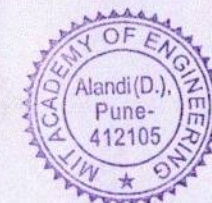
FEBRUARY

S	M	T	W	T	F	S
			1	2	3	4
5	6	7	8	9	10	11
12	13	14	15	16	17	18
19	20	21	22	23	24	25
26	27	28				

Commencement of Term: 1 SEPT	Make-up Sessions: 19 To 21 DEC	Term End: 12 JAN	List of Holidays
SY Project Review I: 30 SEPT	Teaching End - Final Detention: 21 DEC	Result Declaration: 21 JAN	Anant Chaturthi: 9 SEPT
Student's Feedback: 3 OCT	Practical Examination: 22 To 29 DEC	Registration Re-Exam: 23 AND 24 JAN	Dasara: 05 OCT
Mid Semester Exam: 31 OCT To 07 NOV	Project Examination: 24 DEC	Re-Examination: 30 JAN TO 4 FEB	Diwali: 24 and 25 OCT
SY Project Review II: 18 NOV	Preratory Leave: 30 DEC TO 1 JAN	Commencement of Term II: 16 Jan 2023	Christmas: 25 DEC
Provisional Detention List: 15 DEC	End Semester Exam: 2 To 12 JAN	Non-Instructional Days: 24 To 29 OCT	Republic Day: 26 JAN


Proposed By,
Mr. Nilesh Baglekar
Institutional Time table coordinator

Recommended By,
Dr. Sunita Barve and Dr. Anika Kotha
Deputy Director Academics & Research and Controller of Examination

Approved By,
Dr. Mahesh Goudar
Director

2022-2023



MIT

(An Autonomous Institute Affiliated to Savitribai Phule Pune University)

Academy of
Engineering**INSTITUTIONAL ACADEMIC CALENDAR**

Third and Final Year B.Tech (Term I)

JULY

S	M	T	W	T	F	S
					1	2
3	4	5	6	7	8	9
10	11	12	13	14	15	16
17	18	19	20	21	22	23
24	25	26	27	28	29	30
31						

AUGUST

S	M	T	W	T	F	S
	1	2	3	4	5	6
7	8	9	10	11	12	13
14	15	16	17	18	19	20
21	22	23	24	25	26	27
28	29	30				

SEPTEMBER

S	M	T	W	T	F	S
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4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
25	26	27	28	29	30	

OCTOBER

S	M	T	W	T	F	S
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16	17	18	19	20	21	22
23	24	25	26	27	28	29
30	31					

NOVEMBER

S	M	T	W	T	F	S
		1	2	3	4	5
6	7	8	9	10	11	12
13	14	15	16	17	18	19
20	21	22	23	24	25	26
27	28	29	30			

DECEMBER

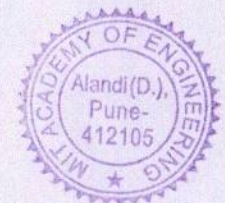
S	M	T	W	T	F	S
				1	2	3
4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
25	26	27	28	29	30	31

Commencement of Term: 1 AUG	Provisional Detention List: 10 NOV	Term End: 17 DEC	List of Holidays
TY Project Review I: 2 SEPT	Make-up Sessions: 14 To 19 NOV	Result Declaration: 26 DEC	Moharum: 9 AUG
B.Tech Project Review I: 16 SEPT	Teaching End - Final Detention: 19 NOV	Registration Re-Exam: 29 To 31 DEC	Independence Day: 15 AUG
Mid Semester Examination: 17 To 24 SEPT	Practical Examination: 21 NOV To 2 DEC	Re-Examination: 5 To 14 Jan 2023	Ganesh Chaturthi: 31 AUG
Student's Feedback: 3 OCT	Project Examination: 26 NOV	Commencement of Term II: 2 Jan 2023	Anant Chaturthi: 9 SEPT
TY Project Review II: 7 OCT	Preratory Leave: 3 To 6 DEC	Non-Instructional Days: 24 To 29 OCT	Dasara: 05 OCT
B.Tech Project Review II: 14 OCT	End Semester Examination: 7 To 17 DEC		Diwali: 24 and 26 OCT

Nilesh Baglekar
Proposed By,
Mr. Nilesh Baglekar
Institutional Time table coordinator

Sunita Barve
Recommended By,
Dr. Sunita Barve and Dr. Anika Kotha
Deputy Director Academics & Research and Controller of Examination

Dr. Manish Goudar
Approved By,
Dr. Manish Goudar
Director

2022-2023

MIT Academy of Engineering			THEORY COURSE RECORD					FACULTY	M. P. Joshi		
AN AUTONOMOUS INSTITUTE			(Teaching Plan)					COURSE	Thermal Engineering		
Alandi (D), Pune - 412105			ACADEMIC YEAR		2017 : 18			LECT/WEEK	4		
DEPARTMENT OF MECHANICAL ENGG.			SEM/TRI		III						
			CLASS		SY BTech						
			Regular/Block		Block II						
Lect. No.	Date of Plan	Topics to be Delivered	ILO	CO No.	No. Lectures Planned	Date of Conduction	Topics Delivered (with reference to column no.3)	No. of Lectures Conducted	Remarks of Faculty (in case of variance)	Monitored by	
										CC	HoD
1	9-Oct-17	1.1 Introduction of thermodynamics, Review of basic definitions	2	1	1	11/10/17	-// -	1			
2	9-Oct-17	1.2 Thermodynamic properties and their units	1	1	1	11/10/17	-// -	1			
3	11-Oct-17	1.3 Zeroth law of thermodynamics, First law of thermodynamics	4	1	1	12/10/17	-// -	1			
4	11-Oct-17	1.4 Steady flow energy equation and its application to different devices	4	2	1	12/10/17	-// -	1			
5	13-Oct-17	1.5 Limitations of First law, Second Law of thermodynamics, Equivalence of Clausius and Kelvin Plank Statement	4	2	1	13/10/17	-// -	1			
6	13-Oct-17	1.6 PMM I and II, Exergy	2	2	1	13/10/17	-// -	1			
7	23-Oct-17	1.7 Review of Heat engine, heat pump and refrigerator.	2	2	1	23/10/17	-// -	1			
8	23-Oct-17	1.9 MCQ Test on UNIT I	-	-	1	23/10/17	-// -	1			
9	24-Oct-17	2.1 Entropy as a property, Clausius inequality	2	1	1	24/10/17	-// -	1			
10	24-Oct-17	2.2 Principle of increase of Entropy	3	1	1	24/10/17	-// -	1			
11	25-Oct-17	2.3 law, Charles's law, Avagadro's Law, Equation of State, Ideal Gas constant and Universal Gas constant	3	2	1	25/10/17	-// -	1			
12	25-Oct-17	2.4 Ideal gas processes- on P-V and T-S diagrams Constant Pressure, Constant Volume	3	2	1	25/10/17	-// -	1			

The record shall be monitored by Class Coordinator (CC) (biweekly) and reviewed by HoD (once a month)

Lect. No.	Date of Plan	Topics to be Delivered	ILO	CO No.	No. Lectures Planned	Date of Conduction	Topics Delivered (with reference to column no.3)	No. of Lectures Conducted	Remarks of Faculty (in case of variance)	Monitored by	
										CC	HoD
13	27-Oct-17	2.6 Polytropic, Throttling Processes	3	2	1	27/10/17	—//—	1			
14	27-Oct-17	2.7 Calculations of heat transfer, work done, internal energy. Change in entropy, enthalpy	4,5	2	1	27/10/17	—//—	1			
15	30-Oct-17	2.8 Availability and Exergy	2	2	1	30/10/17	—//—	1			
16	30-Oct-17	2.9 MCQ Test on UNIT II	—	—	1	30/10/17	—//—	1			
17	31-Oct-17	3.1 Air Standard Cycle, Efficiency and Mean Effective Pressure	3	1	1	31/10/17	—//—	1			
18	31-Oct-17	3.2 Otto Cycle, Diesel cycle	6	3	1	31/10/17	—//—	1			
19	1-Nov-17	3.3 Dual cycle, Comparison of cycles	6	3	1	1/11/17	—//—	1			
20	1-Nov-17	3.4 Brayton cycle, Carnot cycle	3	3	1	1/11/17	—//—	1			
21	3-Nov-17	3.5 Rankine cycle	3	3	1	8/11/17	—//—	1			
22	3-Nov-17	3.6 Comparison of Carnot cycle and Rankine cycle	3	3	1	8/11/17	—//—	1			
23	6-Nov-17	3.7 Introduction of Regeneration, Reheating, and Co-generation	2	3	1	9/11/17	—//—	1	ICE Exam		
24	6-Nov-17	3.8 MCQ Test on UNIT III	—	—	1	9/11/17	—//—	1	ICE Exam		
25	7-Nov-17	4.1 Pure substance, Phases of pure substances	2	1	1	10/11/17	—//—	1			
26	7-Nov-17	4.2 Formation of steam, Properties of steam	2	1	1	10/11/17	—//—	1			
27	8-Nov-17	4.3 Use of Steam Tables, Study of P-V, T-S and Mollier diagram for steam	13	5	1	16/11/17	—//—	1			
28	8-Nov-17	4.4 Dryness fraction and its determination	13	5	1	16/11/17	—//—	1			
29	10-Nov-17	4.5 Study of steam calorimeters	1	1	1	17/11/17	—//—	1			
30	10-Nov-17	4.8 Open Book Test on UNIT IV	—	—	1	17/11/17	—//—	1			
31	13-Nov-17	5.1 Ideal gas mixtures, Combustion, Stoichiometry A/F Ratio	6	1	1	20/11/17	—//—	1	ICE Exam		

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Lect. No.	Date of Plan	Topics to be Delivered		ILO	CO No.	No. Lectures Planned	Date of Conduction	Topics Delivered (with reference to column no.3)	No. of Lectures Conducted	Remarks of Faculty (in case of variance)	Monitored by	
											CC	HoD
32	13-Nov-17	5.2	Product of combustion, Enthalpy of combustion	6	1	1	20/11/17	—//—	1	ICE Exam		
33	14-Nov-17	5.3	Adiabatic Flame temperature, Orsat Apparatus	6	1	1	20/11/17	—//—	1			
34	14-Nov-17	5.4	stages of combustion in SI & CI engine	7	4	1	21/11/17	—//—	1			
35	15-Nov-17	5.5	abnormal combustion, Cooling System	8	4	1	21/11/17	—//—	1			
36	15-Nov-17	5.7	I.C. Engine Emissions and Control	9	1	1	22/11/17	—//—	1			
37	17-Nov-17	5.9	MCQ on UNIT V	—	—	1	22/11/17	—//—	1			
38	17-Nov-17	6.1	Classification and Constructional details of boilers	10	1	1	23/11/17	—//—	1			
39	20-Nov-17	6.2	Mountings and accessories of boiler	11	1	1	23/11/17	—//—	1			
40	20-Nov-17	6.3	Introduction to IBR Act	12	1	1	24/11/17	—//—	1			
41	21-Nov-17	6.4	Boiler draught (natural and artificial draught)	10	5	1	24/11/17	—//—	1			
42	22-Nov-17	6.5	Boiler performance calculations- Equivalent evaporation, Boiler efficiency, Energy balance.	13	5	1	25/11/17	—//—	1			
43	24-Nov-17	6.6	MCQ on UNIT VI	—	—	1	25/11/17	—//—	1			
TOTAL PLANNED						43	TOTAL CONDUCTED		43			

Start of Term (Sign and Date)

End of Term (Sign and Date)

Verified by

The record shall be monitored by Class Coordinator (CC) (biweekly) and reviewed by HoD (once a month)

Lect. No.	Date of Plan	Topics to be Delivered	ILO	CO No.	No. Lectures Planned	Date of Conduction	Topics Delivered (with reference to column no.3)	No. of Lectures Conducted	Remarks of Faculty (in case of variance)	Monitored by	
										CC	HoD
Course Faculty		1712/25			Course Faculty		1712/25				
Class Coordinator		1717			Class Coordinator		1717				
Head of Dept.		P. K. K.			Head of Dept.		P. K. K.				Dean Academics

The record shall be monitored by Class Coordinator (CC) (biweekly) and reviewed by HoD (once a month)

<div><div>MIT</div><div>Academy of Engineering</div><div>(An Autonomous Institute)</div></div>	THEORY COURSE RECORD		COURSE INSTRUCTOR	MANDAR M. BHALEKAR
			COURSE	ELECTRICAL & ELECTRONICS ENGG.
Alandi (D), Pune - 412105	ACADEMIC YEAR	2019-20	CLASS	FYBTech
SCHOOL OF HUMANITIES AND ENGINEERING SCIENCES			DIVISION / BLOCK	A
	SEM / TRI	I	CYCLE	NA
LECT / WEEK			3	

Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
1	1.1 Intro to Course, Energy Scenario, General structure of electrical power systems, A.C. fundamentals, Freq. vary	1	1	19/08/2019	19/08/19	—		
2	1.2 RMS and average value, R-L, phasor diagram, power triangle and power factor,	2	1	22/08/2019	22/8/19	—		
3	1.3 R-C,RLC series and parallel circuits, phasor diagram, power triangle and power factor,	2	1	23/08/2019	23/8/19	—		
4	1.4 measures to improve power factor and its effects on Power system and consumer & Numericals	3	1	26/08/2019	26/8/19	—		
5	1.5 Work, Power & Energy, costing of electricity	4	1	29/08/2019	29/8/19	—		
6	1.6 Application of Renewable Energy Systems, Design of PV system (offgrid),	4	1	30/08/2019	30/8/19	—	#	
7	1.7 Battery selection and its series parallel connections	4	1	05/09/2019	5/9/19	—		
8	2.1 Three phase voltage generation and its waveform, Star balanced systems	5	2	06/09/2019	6/9/19	—		

Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
9	2.2 delta balanced systems, Relationship between phase and line quantities,	6	2	09/09/2019	9/9/19	—		
10	2.3 phasor diagram, power in a three phase circuits,	6	2	13/09/2019	13/9/19	—		
11	2.4 three phase 4 wire system, Difference between neutral and ground conductors,	7	2	16/09/2019	16/9/19	—		
12	2.5 Safety measures in electrical system, types of wiring,	7	2	19/09/2019	19/9/19	—	★	
13	2.6 Active and Reactive Power measurement in single and three phase balanced system.	8	2	20/09/2019	20/9/19	—		
14	2.7 Numericals on three phase connections	8	2	23/09/2019	23/9/19	—		
15	3.1 Rectifiers and Power Supplies, Elements of IC Regulated Power Supply,	9	3	26/09/2019	26/9/19	—		
16	3.2 Numericals on Power supply, Clipper, Clamper.	9	3	27/09/2019	27/9/19	—		
17	3.3 BJT - structure and operation, CE configurations,	10	3	30/09/2019	30/9/19	—	★	
18	3.4 CB, CC configurations, biasing methods, DC Load Line,	10	3	03/10/2019	12/10/19	03 & 04 th Oct - non instructional day		
19	3.5 Analysis & Applications	10	3	04/10/2019	14/10/19	12/10 → Friday TT followed		
20	3.6 Transistor as a switch and Amplifier.	11	3	14/10/2019	17/10/19			
21	3.7 operation and application as a switch. Opto-electronic devices – Photo conductive cell, Photo Voltaic cell.	11	3	17/10/2019	17/10/19			
22	4.1 Graded Quiz on Unit-3, Digital: Logic gates, Boolean algebra, Kmap, SOP representation	12	4	18/10/2019	18/10/19			

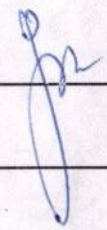
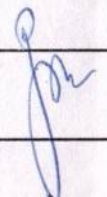
Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
23	4.2 Combinational circuit Design: Adder, Subtractor,	13	4	21/10/2019	04/11/19			
24	4.3 MUX, DMUX, Comparator,	13	4	24/10/2019	07/11/19			
25	4.4 Code converter, Applications	13	4	25/10/2019	08/11/19	08/11 - Kartiki holiday is cancelled.		
26	4.5 Sequential circuit: Flip-Flop, Registers	14	4	04/11/2019	11/11/19			
27	4.6 Synchronous & Asynchronous Counters.	14	4	07/11/2019	18/11/19	14/11 → Adj. to SR 15/11 → Adj. to DBP		
28	4.7 Microprocessor and Microcontroller based systems.	15	4	11/11/2019	21/11/19			
29	5.1 Elements of measuring system, Sensors	16	5	14/11/2019	22/11/19	} exam		
30	5.2 Sensors & Transducers -Temperature, Flow	16	5	15/11/2019	21/11/19			
31	5.3 Sensors & Transducers -Pressure, level, IR, Speed & LVDT.	16	5	18/11/2019	22/11/19			
32	5.4 Op-Amp - IC 741 pin configuration, Op-amp parameters, Inverting,	17	5	21/11/2019	25/11/19			
33	5.5 Non- Inverting & Differential configuration	17	5	22/11/2019	28/11/19			
34	5.6 Applications: Summing & Difference amplifier, Comparator, Voltage follower.	18	5	25/11/2019	29/11/19			
35	6.1 Construction of Transformer, principle of operation, EMF equation, VA Rating,	19	6	28/11/2019	29/11/19			
36	6.2 Efficiency and Voltage regulation, OC/SC Test on Transformer	19	6	29/11/2019	02/12/19			
37	6.3 Construction, principle of operation and types of DC motor, Speed Control, characteristics equation	20	6	02/12/2019	05/12/19			

Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
38	6.4 PMDC, BLDC, Universal motor, Single phase Induction Motor,	21	6	05/12/2019	06/12/19	—	} A	Jm
39	6.5 Stepper motor, Application of Electrical Motors in domestic and Industrial sector, Numericals on Motor	22	6	06/12/2019	—	—		

TOTAL LECTURES PLANNED		39	TOTAL LECTURES CONDUCTED		38
Start of Term (Sign and Date)		End of Term (Sign and Date)			Verified by
Course Instructor	<u>Atalhar</u> 19/8/19	Course Instructor	<u>Atalhar</u> 6/12/19		<u>SSBore</u> 6/12/19
Class Coordinator / Instructor Incharge	<u>Atalhar</u> 19/8/19	Class Coordinator / Instructor Incharge	<u>Atalhar</u> 06/12/19		
Dean	<u>Dr</u> 19/8/19	Dean	<u>Dr</u> 6/12/19		Dean - Academics

<div>MIT</div> <div>Academy of Engineering</div> <div>(An Autonomous Institute)</div>	THEORY COURSE RECORD		COURSE INSTRUCTOR	MANDAR M. BHALEKAR
			COURSE	ELECTRICAL & ELECTRONICS ENGG.
Alandi (D), Pune - 412105	ACADEMIC YEAR	2019-20	CLASS	FYBTech
SCHOOL OF HUMANITIES AND ENGINEERING SCIENCES			DIVISION / BLOCK	B
	SEM / TRI	I	CYCLE	NA
LECT / WEEK			3	

Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
1	1.1 Intro to Course, Energy Scenario, General structure of electrical power systems, A.C. fundamentals, Freq. vary	1	1	19/08/2019	19/8/19	—		
2	1.2 RMS and average value, R-L, phasor diagram, power triangle and power factor,	2	1	22/08/2019	22/8/19	—		
3	1.3 R-C,RLC series and parallel circuits, phasor diagram, power triangle and power factor,	2	1	22/08/2019	22/8/19	—		
4	1.4 measures to improve power factor and its effects on Power system and consumer & Numericals	3	1	26/08/2019	26/8/19	—		
5	1.5 Work, Power & Energy, costing of electricity	4	1	27/08/2019	27/8/19	—		
6	1.6 Application of Renewable Energy Systems, Design of PV system (offgrid),	4	1	29/08/2019	29/8/19	—		
7	1.7 Battery selection and its series parallel connections	4	1	03/09/2019	3/9/19	—		
8	2.1 Three phase voltage generation and its waveform, Star balanced systems	5	2	05/09/2019	5/9/19	—		

Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
9	2.2 delta balanced systems, Relationship between phase and line quantities,	6	2	09/09/2019	9/9/19	—		
10	2.3 phasor diagram, power in a three phase circuits,	6	2	10/09/2019	10/9/19	—		
11	2.4 three phase 4 wire system, Difference between neutral and ground conductors,	7	2	16/09/2019	16/9/19	—	AA	
12	2.5 Safety measures in electrical system, types of wiring,	7	2	17/09/2019	17/9/19	—		
13	2.6 Active and Reactive Power measurement in single and three phase balanced system.	8	2	19/09/2019	19/9/19	—		
14	2.7 Numericals on three phase connections	8	2	23/09/2019	23/9/19	—		
15	3.1 Rectifiers and Power Supplies, Elements of IC Regulated Power Supply,	9	3	24/09/2019	24/9/19	—		
16	3.2 Numericals on Power supply, Clipper, Clamper.	9	3	26/09/2019	26/9/19	—		
17	3.3 BJT - structure and operation, CE configurations,	10	3	30/09/2019	30/9/19	—	AA	
18	3.4 CB, CC configurations, biasing methods, DC Load Line,	10	3	01/10/2019	01/10/19	—		
19	3.5 Analysis & Applications	10	3	03/10/2019	12/10/19	03/10/19 - non instructor day.		
20	3.6 Transistor as a switch and Amplifier.	11	3	14/10/2019	14/10/19			
21	3.7 operation and application as a switch. Opto-electronic devices – Photo conductive cell, Photo Voltaic cell.	11	3	15/10/2019	15/10/19			

Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
22	4.1 Graded Quiz on Unit-3, Digital: Logic gates, Boolean algebra, Kmap, SOP representation	12	4	17/10/2019	17/10/19			
23	4.2 Combinational circuit Design: Adder, Subtractor,	13	4	21/10/2019	17/10/19			
24	4.3 MUX, DMUX, Comparator,	13	4	22/10/2019	04/10/19			
25	4.4 Code converter, Applications	13	4	24/10/2019	05/11/19			
26	4.5 Sequential circuit: Flip-Flop, Registers	14	4	04/11/2019	07/11/19			
27	4.6 Synchronous & Asynchronous Counters.	14	4	05/11/2019	11/11/19			
28	4.7 Microprocessor and Microcontroller based systems.	15	4	07/11/2019	12/11/19			
29	5.1 Elements of measuring system, Sensors	16	5	11/11/2019	18/11/19			
30	5.2 Sensors & Transducers -Temperature, Flow	16	5	12/11/2019	21/11/19	19/11 → CL adj. to DPB		
31	5.3 Sensors & Transducers -Pressure, level, IR, Speed & LVDT.	16	5	14/11/2019	25/11/19			
32	5.4 Op-Amp - IC 741 pin configuration, Op-amp parameters, Inverting,	17	5	18/11/2019	26/11/19			
33	5.5 Non- Inverting & Differential configuration	17	5	19/11/2019	28/11/19			
34	5.6 Applications: Summing & Difference amplifier, Comparator, Voltage follower.	18	5	21/11/2019	28/11/19			
35	6.1 Construction of Transformer, principle of operation, EMF equation, VA Rating,	19	6	25/11/2019	28/11/19			
36	6.2 Efficiency and Voltage regulation, OC/SC Test on Transformer	19	6	26/11/2019	02/12/19			

Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
37	6.3 Construction, principle of operation and types of DC motor,	20	6	28/11/2019	03/12/19	-		
38	6.4 Speed Control, characteristics equation,	20	6	02/12/2019	05/12/19	-		
39	6.5 PMDC, BLDC, Universal motor, Single phase Induction Motor,	21	6	03/12/2019	-	-		
40	6.6 Stepper motor, Application of Electrical Motors in domestic and Industrial sector, Numericals on Motor	22	6	05/12/2019	-	-		
TOTAL LECTURES PLANNED		40		TOTAL LECTURES CONDUCTED		38		

Start of Term (Sign and Date)		End of Term (Sign and Date)		Verified by
Course Instructor	<i>Atalhar</i> 19/8/19	Course Instructor	<i>Atalhar</i> 05/12/19	<i>Sobane</i> 5/12/19
Class Coordinator / Instructor Incharge	<i>Atalhar</i> 19/8/19	Class Coordinator / Instructor Incharge	<i>Atalhar</i> 5/12/19	
Dean	<i>Dr</i> 19/8/19	Dean	<i>Dr</i> 5/12/19	
				Dean - Academics

MIT Academy of Engineering [An Autonomous Institute] Alandi (D), Pune - 412105 SCHOOL OF HUMANITIES AND ENGINEERING SCIENCES	THEORY COURSE RECORD		COURSE INSTRUCTOR	A J Asalekar
			COURSE	ENGINEERING GRAPHICS
			CLASS	FY BTECH
			DIVISION	F
			CYCLE	NA
		LECT / WEEK	2	
		ACADEMIC YEAR	2018-19	
		SEMESTER	II	

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - in case of variance)	Monitored by	
								CC / II	Dean
1	0.1	Essentials of Engineering Graphics including technical drawing (Squiggle Bird)	1	1	07-01-2019	14-01-2019	Adjusted with Azhar Sheikh sir		
2	1.1	projection of Points	1	1	07-01-2019	14-01-2019			
3	1.2	projection of Lines	2	2	14-01-2019	21-01-2019			
4	1.3	projection of Plane	2	2	14-01-2019	21-01-2019			
5	1.4	projection of Solides (Activity- Poster Presentation)	3	2	21-01-2019	25-01-2019			
6	1.5	Examples of Projections of Line Plane & Solids	3	2	21-01-2019	25-01-2019			
7	2.1	Types of Orthographic Projections	3	3	28-01-2019	04-02-2019			
8	2.2	1 st and 3 rd angle projection system	3	3	28-01-2019	04-02-2019			
9	2.3	Dimensioning Components	2	3	04-02-2019	18-02-2019			
10	2.4	Sectional Views	3	3	04-02-2019	18-02-2019			
11	2.5	Orthographic Projection of Mechanical Components	4	3	18-02-2019	25-02-2019			

The record shall be monitored by Class Coordinator (CC) / Instructor Incharge (II) (biweekly) and reviewed by Dean (once a month)

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - in case of variance)	Monitored by	
								CC / II	Dean
12	3.1	Introduction to Isometric Views	5	4	18-02-2019	25-02-2019	-		
13	3.2	Construction of Isometric View from given Orthographic View	5	4	25-02-2019	26-02-2019	-		
14	3.3	Dimensioning in Isometric Projection	4	4	25-02-2019	01-03-2019	-		
15	3.4	Isometric View of Pyramis, Cone & Sphere (Think Pair Share)	6	4	04-03-2019	01-03-2019	-		
16	3.5	Isometric View of Mechanical Engineering Components	7	4	04-03-2019	11-03-2019	-		
17	4.1	Identification of Lines/ Edges & Surfaces	1	5	11-03-2019	11-03-2019	-		
18	4.2	Adding missing/ third view	9	5	11-03-2019	12-03-2019	-		
19	4.3	Adding Sectional View	8	5	18-03-2019	18-03-2019	-		
20	4.4	Convert given view into Sectional View	9	3	18-03-2019	22-03-2019	-		
21	5.1	Auxiliary Planes	6	3	25-03-2019	22-03-2019	-		
22	5.2	AVP & AIP	6	4	25-03-2019	08-04-2019	-		
23	5.3	Symmetrical Auxiliary View	8	4	01-04-2019	08-04-2019	-		
24	5.4	Unilateral & Bilateral Auxiliary View	8	4	01-04-2019	12-04-2019	-		
25	6.1	FV & TV of Standard Machine Parts Shaft, Keys, Couplings	7	6	08-04-2019	12-04-2019	-		
26	6.2	Springs, Screw thread forms	7	6	08-04-2019	26-04-2019	-		
27	6.3	Welded Joints,	7	6	15-04-2019	26-04-2019	-		
28	6.4	Riveted Joints (Activity Jigsaw) (1 Minute Dart Board Activity)	7	6	15-04-2019	26-04-2019	-		

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - in case of variance)	Monitored by	
								CC / II	Dean
30									
31									
32									
33		Field Visit				14/04/2019	-		
34									

Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
30								
31								
32								
33	Field Visit				14/04/2019			
34								

TOTAL LECTURES PLANNED	28
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TOTAL LECTURES CONDUCTED	
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Start of Term (Sign and Date)	
Course Instructor	
Class Coordinator / Instructor Incharge	
Dean	

End of Term (Sign and Date)	
Course Instructor	
Class Coordinator / Instructor Incharge	
Dean	

Verified by
Dean - Academics

Seen
/

The record shall be monitored by Class Coordinator (CC) / Instructor Incharge (II) (biweekly) and reviewed by Dean (once a month)

MIT Academy of Engineering (An Autonomous Institute)		THEORY COURSE RECORD				COURSE INSTRUCTOR		
Alandi (D), Pune - 412105		ACADEMIC YEAR		2018-19		COURSE	ENGINEERING GRAPHICS	
SCHOOL OF HUMANITIES AND ENGINEERING SCIENCES		SEMESTER		II		CLASS	FY BTECH	
						DIVISION	E	
						CYCLE	NA	
						LECT / WEEK	2	

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - in case of variance)	Monitored by	
								CC / II	Dean
1	0.1	Essentials of Engineering Graphics including technical drawing (Squiggle Bird)	1	1	09-01-2019	09-01-2019	-		
2	1.1	projection of Points	1	1	09-01-2019	09-01-2019	-		
3	1.2	projection of Lines	2	2	16-01-2019	16-01-2019	-		
4	1.3	projection of Plane	2	2	16-01-2019	16-01-2019	-		
5	1.4	projection of Solides (Activity- Poster Presentation)	3	2	23-01-2019	23-01-2019	-		
6	1.5	Examples of Projections of Line Plane & Solids	3	2	23-01-2019	23-01-2019	-		
7	2.1	Types of Orthographic Projections	3	3	30-01-2019	30-01-2019	-		
8	2.2	1 st and 3 rd angle projection system	3	3	30-01-2019	30-01-2019	-		
9	2.3	Dimensioning Components	2	3	06-02-2019	06-02-2019	-		
10	2.4	Sectional Views	3	3	06-02-2019	06-02-2019	-		
11	2.5	Orthographic Projection of Mechanical Components	4	3	20-02-2019	20-02-2019	-		

The record shall be monitored by Class Coordinator (CC) / Instructor Incharge (II) (biweekly) and reviewed by Dean (once a month)

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
								CC / II	Dean
12	3.1	Introduction to Isometric Views	5	4	20-02-2019	26-2-2019			
13	3.2	Construction of Isometric View from given Orthographic View	5	4	27-02-2019	24-2-2019			
14	3.3	Dimensioning in Isometric Projection	4	4	27-02-2019	29-2-2019			
15	3.4	Isometric View of Pyramid, Cone & Sphere (Think Pak Share)	6	4	06-03-2019	06-03-2019			
16	3.5	Isometric View of Mechanical Engineering Components	7	4	06-03-2019	06-03-2019			
17	4.1	Identification of Lines/ Edges & Surfaces	1	5	13-03-2019	13-03-2019			
18	4.2	Adding missing/ third view	9	5	13-03-2019	13-03-2019			
19	4.3	Adding Sectional View	8	5	20-03-2019	20-03-2019			
20	4.4	Convert given view into Sectional View	9	3	20-03-2019	20-03-2019			
21	5.1	Auxiliary Planes	6	3	27-03-2019	27-03-2019			
22	5.2	AVP & AIP	6	4	27-03-2019	27-03-2019			
23	5.3	Symmetrical Auxiliary View	8	4	03-04-2019	03-04-2019			
24	5.4	Unilateral & Bilateral Auxiliary View	8	4	03-04-2019	03-04-2019			
25	6.1	FV & TV of Standard Machine Parts Shaft, Keys, Couplings	7	6	10-04-2019	10-04-2019			
26	6.2	Springs, Screw thread forms	7	6	10-04-2019	10-04-2019			
27	6.3	Welded Joints,	7	6	17-04-2019	10-04-2019			
28	6.4	Riveted Joints (Activity Jigsaw) (1 Minute Dart Board Activity)	7	6	17-04-2019	24-04-19			

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
								CC / II	Dean
30									
31									
32									
33		Field Visit				14-04-2019			
34									

TOTAL LECTURES PLANNED 28

TOTAL LECTURES CONDUCTED

Start of Term (Sign and Date)	
Course Instructor	
Class Coordinator / Instructor Incharge	
Dean	

End of Term (Sign and Date)	
Course Instructor	
Class Coordinator / Instructor Incharge	
Dean	





Verified by
Dean - Academics

MIT Academy of Engineering (An Autonomous Institute) SCHOOL OF ELECTRICAL ENGG.	THEORY COURSE RECORD		COURSE INSTRUCTOR	ASHISH SRIVASTAVA
			COURSE	VLSI DESIGN
	Alandi (D), Pune - 412105	ACADEMIC YEAR	CLASS	B.TECH E&TC
		2019-20	DIVISION / BLOCK	IV
		SEM / TRI	CYCLE	NA
		VII	LECT / WEEK	3

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
								CC / II	Dean
1	1.1	Introduction to VLSI Design	1	1	05/08/2019	13/08/2019	05.06 Holiday in view of rain.	}	
2	1.2	MOS Transistors, CMOS Fabrication	1	1	06/08/2019	13/08/2019			
3	1.3	CMOS Inverter	2	1	06/08/2019	19/08/2019			
4	1.4	CMOS combinational Logic, Transistor Sizing	3	1	13/08/2019	20/08/2019			
5	1.5	Power Dissipation, Noise Margin	4	1	13/08/2019	20/08/2019		}	
6	1.6	Body Effect, Short Channel & Hot Electron Effect	5	1	19/08/2019	26/08/2019			
7	1.7	Transmission Gate, Activity	6	1	20/08/2019	27/08/2019			
8	1.8	Technology Scaling,	6	1	20/08/2019	27/08/2019			
9	1.9	Design Rules, Stick Diagram, Revision	6	1	26/08/2019	03/09/2019		}	
10	2.1	Need of PLDs, ASIC,	7	2	27/08/2019	03/09/2019			
11	2.2	Programmable logic devices, Types of PLDs,	7	2	27/08/2019	03/09/2019			
12	2.3	Advantages & Disadvantages of PLDs	7	2	03/09/2019	10/09/2019	Extra		

<div><div>MIT</div><div>Academy of Engineering</div><div>(An Autonomous Institute)</div></div>	THEORY COURSE RECORD		COURSE INSTRUCTOR	ASHISH SRIVASTAVA	
			COURSE	VLSI DESIGN	
	Alandi (D), Pune - 412105	ACADEMIC YEAR	2019-20	CLASS	B.TECH E&TC
	SCHOOL OF ELECTRICAL ENGG.	SEM / TRI	VII	DIVISION / BLOCK	IV
				CYCLE	NA
			LECT / WEEK	3	

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - In case of variance)	Monitored by	
								CC / II	Dean
1	1.1	Introduction to VLSI Design	1	1	05/08/2019	13/08/2019	05.06 Holiday in view of rain.	}	
2	1.2	MOS Transistors, CMOS Fabrication	1	1	06/08/2019	13/08/2019			
3	1.3	CMOS Inverter	2	1	06/08/2019	19/08/2019			
4	1.4	CMOS combinational Logic, Transistor Sizing	3	1	13/08/2019	20/08/2019			
5	1.5	Power Dissipation, Noise Margin	4	1	13/08/2019	20/08/2019		}	
6	1.6	Body Effect, Short Channel & Hot Electron Effect	5	1	19/08/2019	26/08/2019			
7	1.7	Transmission Gate, Activity	6	1	20/08/2019	27/08/2019			
8	1.8	Technology Scaling,	6	1	20/08/2019	27/08/2019			
9	1.9	Design Rules, Stick Diagram, Revision	6	1	26/08/2019	03/09/2019		}	
10	2.1	Need of PLDs, ASIC,	7	2	27/08/2019	03/09/2019			
11	2.2	Programmable logic devices, Types of PLDs,	7	2	27/08/2019	03/09/2019			
12	2.3	Advantages & Disadvantages of PLDs	7	2	03/09/2019	10/09/2019	Extra		

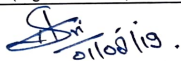


Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - in case of variance)	Monitored by	
								CC / II	Dean
13	2.4	Complex PLDs	9	2	03/09/2019	10/9/2019		} 	
14	2.5	Field Programmable Gate Arrays (FPGAs)	8,9	2	09/09/2019	10/9/2019			
15		Guest Lecture			10/09/2019	16/9/2019			
16	2.6	IFPGAs Technology & mplementing Functions	8,10	2	10/09/2019	17/9/2019		}	
17	2.7	Case study: Xilinx Artix-7 FPGA family, Activity	10	2	16/09/2019	17/9/2019			
18	3.1	Elements of VHDL	11	3	17/09/2019	23/9/2019			
19	3.2	Modeling Styles	11	3	17/09/2019	24/9/2019			
20	3.3	Sequential Statements, Concurrent Statements	12	3	23/09/2019	24/9/2019			
21	3.4	Packages, Sub Programs, Attributes	11	3	24/09/2019	25/9/2019	} Extra.	}	
22	3.5	VHDL Modeling Of Combinational Logic	13	3	24/09/2019	25/9/2019			
23	3.5	VHDL Modeling Of Combinational Logic	13	3	30/09/2019	26/9/2019	} Extra.		
24	3.6	Sequential Logics & State Machine Modeling of Mealy Machine)	14	3	01/10/2019	26/9/2019			
25	3.6	Sequential Logics & State Machine Modeling of Mealy Machine)	14	3	01/10/2019	7/10/2019			
26	3.7	VHDL Test Bench, Activity	13,14	3	07/10/2019	14/10/2019			
27	4.1	Overview of Digital Design with Verilog HDL	15	4	14/10/2019	15/10/2019			
28	4.2	Program Structure, Logic System	15	4	15/10/2019	15/10/2019			
29	4.3	Nets, Variables & Constants	15	4	15/10/2019	19/10/2019			

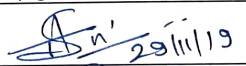


Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
								CC / II	Dean
30	4.4	Vectors & Operators, Arrays, Logical Operators & Expressions	15	4	21/10/2019	22/10/2019		}	
31	4.5	Hierarchical Modeling Concepts	17	4	22/10/2019	22/10/2019			
32	4.6	Data types, Modules and Ports	17	4	22/10/2019	04/11/2019			
33	4.7	Modeling Styles data flow, Activity	16	4	04/11/2019	05/11/2019			
34	4.8	Modeling Styles behavioural, sequential	16	4	05/11/2019	05/11/2019			
35	5.1	Types of Fault, Need of Design for Testability (DFT)	19	5	05/11/2019	11/11/2019			
36	5.2	Testability, Difference between testing and verification	19	5	11/11/2019	12/11/2019			
37	5.3	Path Sensitizing, Sequential Circuit Test	20	5	12/11/2019	12/11/2019			
38	5.4	BIST, Test Pattern Generation	20	5	12/11/2019	18/11/2019			
39	5.5	JTAG & Boundary Scan	21	5	18/11/2019	19/11/2019			
40	5.6	TAP Controller	21	5	19/11/2019	19/11/2019		}	
41	5.7	Metastability and Solution	22	5	19/11/2019	19/11/2019	Class Test.		
42	5.8	Activity		5	25/11/2019	25/11/2019	Pract.		
43		Practical exam			26/11/2019	25/11/2019	Pract.		
44					26/11/2019				
45									

Lect. No.	Topics to be Delivered	TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
							CC / II	Dean
46								

TOTAL LECTURES PLANNED	43
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TOTAL LECTURES CONDUCTED	43.
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Start of Term (Sign and Date)	
Course Instructor	 01/08/19.
Class Coordinator / Instructor Incharge	 11/8/19
Dean	 07/8

End of Term (Sign and Date)	
Course Instructor	 29/11/19.
Class Coordinator / Instructor Incharge	 28.11
Dean	 29/11

Verified by
Dean - Academics

MIT Academy of Engineering [An Autonomous Institute] Alandi (D), Pune - 412105 SCHOOL OF ELECTRICAL ENGG.	LAB COURSE RECORD		COURSE INSTRUCTOR	ASHISH SRIVASTAVA
			COURSE	VLSI Design
	ACADEMIC YEAR	2019-20	CLASS	BTECH E&TC
	SEM / TRI	VII	DIVISION / BLOCK	Block-IV, Batch 1
			CYCLE	NA
			HRS / WEEK	2

EXPT	Experiment Planned		CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
							CC / II	Dean
A	A1	Design and implementation of Layout of Inverter using CMOS 0.25 micron Technology in Microwind.	1	07/08/2019	—	No student	} <u>21/8</u>	
	A2	Design and implementation of Layout of NAND using CMOS 0.25 micron Technology in Microwind.		14/08/2019	14/08/2019.			
	A3	Design and implementation of Layout of NOR using CMOS 0.25 micron Technology in Microwind.		21/08/2019	21/08/2019			
B	B1	Design and implementation of Layout of Full Adder using CMOS 0.25 micron Technology in Microwind.	1	28/08/2019	28/08/2019		} <u>21/8</u> 11/9	✓
	B2	Design and implementation of Layout of Multiplexer using CMOS 0.25 micron Technology in Microwind.		04/09/2019	04/09/2019			
	B3	Design and implementation of Layout of Demultiplexer using CMOS 0.25 micron Technology in Microwind.		11/09/2019	11/09/2019.			
C	C1	Design and implementation of Layout of 2:1 Multiplexer using logic gates	1	18/09/2019	18/09/2019		} <u>21/8</u> 11/9	✓
	C2	Design and implementation of Layout of 2:1 Multiplexer using transmission gates		09/10/2019	25/09/2019.			
D	Write a VHDL code for Mealy State Machine/Moore State Machine-examples		3	16/10/2019	09/10/2019			✓
E	Universal shift register with mode selection input for SISO, SIPO, PISO, & PIPO modes/FIFO		3	23/10/2019	23/10/2019			




EXPT	Experiment Planned	CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
						CC / II	Dean
F	Write VHDL code to generate ramp/square waveform using DAC /seven segment display / keypad interfacing.	3	30/10/2019	06/11/19		} Eng	
G	Write a Verilog code for Full adder.	3	06/11/2019	13/11/19			
H	Write a Verilog code for counter	3	13/11/2019	20/11/19			
	Revision		20/11/2019	-			
	Practical		27/11/2019	-	Pract. Exam on 25/11		
I							

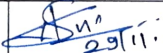

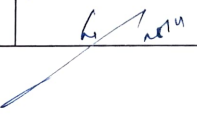
TOTAL EXPT. PLANNED

8

TOTAL EXPT. CONDUCTED

8

Start of Term (Sign and Date)	
Course Instructor	
Class Coordinator / Instructor Incharge	
Dean	

End of Term (Sign and Date)	
Course Instructor	
Class Coordinator / Instructor Incharge	
Dean	

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Dean - Academics



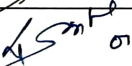
MIT Academy of Engineering [An Autonomous Institute] Alandi (D), Pune - 412105 SCHOOL OF ELECTRICAL ENGG.	LAB COURSE RECORD		COURSE INSTRUCTOR	ASHISH SRIVASTAVA
			COURSE	VLSI DESIGN
	ACADEMIC YEAR	2019-20	CLASS	BTECH ETX
	SEM / TRI	VII	DIVISION / BLOCK	Block-II, Batch 2
			CYCLE	NA
			HRS / WEEK	2



EXPT	Experiment Planned		CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
							CC / II	Dean
A	A1	Design and implementation of Layout of Inverter using CMOS 0.25 micron Technology in Microwind.	1	05/08/2019	—		[Signature]	[Signature]
	A2	Design and implementation of Layout of NAND using CMOS 0.25 micron Technology in Microwind.		19/08/2019	19/08/2019			
	A3	Design and implementation of Layout of NOR using CMOS 0.25 micron Technology in Microwind.		26/08/2019	26/08/2019			
B	B1	Design and implementation of Layout of Full Adder using CMOS 0.25 micron Technology in Microwind.	1	09/09/2019	09/09/2019		[Signature]	[Signature]
	B2	Design and implementation of Layout of Multiplexer using CMOS 0.25 micron Technology in Microwind.		16/09/2019	16/09/2019			
	B3	Design and implementation of Layout of Demultiplexer using CMOS 0.25 micron Technology in Microwind.		30/09/2019	23/09/2019			
C	C1	Design and implementation of Layout of 2:1 Multiplexer using logic gates	1	07/10/2019	07/10/2019		[Signature]	[Signature]
	C2	Design and implementation of Layout of 2:1 Multiplexer using transmission gates		14/10/2019	14/10/2019			
D		Write a VHDL code for Mealy State Machine/Moore State Machine-examples	3	21/10/2019	19/10/2019	Satwarking.	[Signature]	[Signature]
E		Universal shift register with mode selection input for SISO, SIPO, PISO, & PIPO modes/FIFO	3	04/11/2019	04/11/2019		[Signature]	[Signature]

EXPT	Experiment Planned	CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
						CC / II	Dean
F	Write VHDL code to generate ramp/square waveform using DAC /seven segment display / keypad interfacing.	3	11/11/2019	11/11/2019			
G	Write a Verilog code for Full adder.	3	18/11/2019	14/11/2019	G.K.R		
H	Write a Verilog code for counter	3	25/11/2019	18/11/2019			

TOTAL EXPT. PLANNED	8
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TOTAL EXPT. CONDUCTED	8
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Start of Term (Sign and Date)	
Course Instructor	 01/08/19.
Class Coordinator / Instructor Incharge	 01/08/19
Dean	 01/8

End of Term (Sign and Date)	
Course Instructor	 01/10/19.
Class Coordinator / Instructor Incharge	
Dean	 01/10/19

Verified by
Dean - Academics

MIT Academy of Engineering (An Autonomous Institute) Alandi (D), Pune - 412105 SCHOOL OF ELECTRICAL ENGG.	THEORY COURSE RECORD		COURSE INSTRUCTOR	PRACHI RAJARAPOLLU
			COURSE	DSA
	ACADEMIC YEAR	2019-20	CLASS	SY BTECH
	SEM / TRI	III	DIVISION / BLOCK	B3
			CYCLE	NA
			LECT / WEEK	3

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - in case of variance)	Monitored by	
								CC / II	Dean
1	1.1	Unit I: Introduction to Number system, combinational logic design	1	1	2.8.19	9.8.19			
2	1.2	BCD Arithmetic operations	1	1	5.8.19	16.8.19	Non instructional Working day due to rain.		
3	1.3	Digital Codes and Parity	2	1	5.8.19	16.8.19			
4	1.4	Digital Codes and Parity	2	1	9.8.19	19.8.19			
5	1.5	Parity generators/checkers	3	1	16.8.19	19.8.19			
6	1.6	Applications of data selector & distributor	4	1	19.8.19	21.8.19	Extra lec against 5th Aug.		
7	2.1	Unit II : Shift Registers & its applications	5	2	19.8.19	21.8.19			

The record shall be monitored by Class Coordinator (CC) / Instructor Incharge (II) (biweekly) and reviewed by Dean (once a month)

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - in case of variance)	Monitored by	
								CC / II	Dean
8	2.2	Shift Registers & its applications	5	2	23.8.19	26.8.19			
9	2.3	Pulse train generators	6	2	26.8.19	26.8.19			
10	2.4	Counters and its design	7	2	26.8.19	30.8.19			
11	2.5	Counters and its design	7	2	30.8.19	30.8.19			
12	2.6	Counting Real world events	8	2	6.9.19	6.9.19			
		Graded Activity 1- Assignment - Online submission and assessment	1 to 8	1, 2	6.9.19	6.9.19			
13	3.1	Unit III : Sequence detector	9	3	9.9.19	6.9.19			
14	3.2	Sequence detector	9	3	9.9.19	9.9.19			
15	3.3	State diagram: U/D decade counter	10	3	13.9.19	9.9.19			
16	3.4	State diagram: U/D decade counter	10	3	16.9.19	16.9.19			
17	3.5	Traffic Light Controller & ATM machines	11	3	16.9.19	16.9.19			
18		Graded Activity 2 - Flipped class room - Inclass quiz on given video on moodle	All	1,2,3	20.9.19	20.9.19			
19	4.1	Unit IV: Classification and characteristics of logic families	12	4	23.9.19	23.9.19			
20	4.2	Classification and characteristics of logic families	12	4	23.9.19	23.9.19			

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - in case of variance)	Monitored by	
								CC / II	Dean
21	4.3	Design and functioning of ROM	13	4	27.9.19	27.9.19			
22	4.4	Design and functioning of PAL	13	4	7.10.19	12.10.19			
23	4.5	Design and functioning of PLA	13	4	7.10.19	14.10.19			
24	4.6	Interfacing CMOS & TTL with LEDs & Switches	14	4	14.10.19	14.10.19			
		Graded Activity 3- Quiz- Online submission and assessment	9 to 14	3 & 4	14.10.19	14.10.19			
25	5.1	Unit V : Basic constructs & Programming using VHDL	15	5	14.10.19	17.10.19			
26	5.2	Basic constructs & Programming using VHDL	15	5	18.10.19	19.10.19			
27	5.3	Design a system to control LED's through switches	16	5	21.10.19	19.10.19			
28	5.4	Design a system to control LED's through switches	16	5	21.10.19	4.11.19			
29	5.5	Design of combinational circuits using VHDL	17	5	25.10.19	4.11.19			
30	6.1	Unit VI : Microprocessor evolution, Comparison between RISC and CISC	18	6	4.11.19	8.11.19			
31	6.2	Microprocessor evolution, Comparison between RISC and CISC	18	6	4.11.19	8.11.19	Guest lecture		
32	6.3	Architecture of general purpose and special purpose processor	19	6	11.11.19	11.11.19			
33	6.4	Case study: Data acquisition system	20	6	11.11.19	11.11.19			

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
								CC / II	Dean
34	6.5	Case study: Data acquisition system	20	6	15.11.19	18.11.19			
		Graded activity 4 - Case study - Online submission and assessment	20	6	11.11.19	18.11.19			
35		Graded activity 5 - Mind map presentation	All	All	15.11.19	18.11.19			
36		Graded activity 5- Mind map presentation	All	All	18.11.19	05.11.19			
37		Revision on design, implementation and application on above topics	All	All	18.11.19	20.11.19			
38		Revision on design, implementation and application on above topics	All	All	22.11.19	20.11.19			

TOTAL LECTURES PLANNED

38

TOTAL LECTURES CONDUCTED

38

Start of Term (Sign and Date)

Course Instructor	<i>PRP</i> 11/8/19
Class Coordinator / Instructor Incharge	<i>PRP</i> 11/8/19
Dean	<i>PRP</i> 01/8

End of Term (Sign and Date)

Course Instructor	<i>PRP</i> 29/11/19
Class Coordinator / Instructor Incharge	<i>PRP</i> 30/11/19
Dean	<i>PRP</i> 30/11

Verified by

Dean - Academics

The record shall be monitored by Class Coordinator (CC) / Instructor Incharge (II) (biweekly) and reviewed by Dean (once a month)

MIT Academy of Engineering [An Autonomous Institute] Alandi (D), Pune - 412105 SCHOOL OF ELECTRICAL ENGG.	LAB COURSE RECORD		COURSE INSTRUCTOR	PRACHI RAJARAPOLLU
			COURSE	DSA
	ACADEMIC YEAR	2019-20	CLASS	SY BTECH
	SEM / TRI	III	DIVISION / BLOCK	B3, Batch I
			CYCLE	NA
			HRS / WEEK	2

EXPT	Experiment Planned		CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
							CC / II	Dean
A		Code Converter using IC 74HC154	1			6.8.19 - non instructional working day.	AC	bn
	A1	Design and Implement 4 bit Binary to Grey Code Converter vice versa		6.8.19	13.8.19			
	A2	Design and Implement multiple output logic function using IC 74HC154		13.8.19	20.8.19			
B		Parity Generators & Checkers	1				AC	
	B1	Design and Implement 8,9 and 16 bit Even/Odd Parity Generator using IC 74LS180		20.8.19	20.8.19			
	B2	Design and Implement 8,9 and 16 bit Even/Odd Parity Checker using IC 74LS180		20.8.19	20.8.19			
C		Adders and Subtractors	1					
	C1	Design and Implement 1 digit BCD adder, subtractor using IC 74LS83		27.8.19	27.8.19			
D		Synchronous Counters	2				AC	bn
	D1	Design and Implement MOD N and MOD NN Binary counters using IC 74LS163		3.9.19	3.9.19			
	D2	Design and Implement MOD N and MOD NN BCD counters using IC 74LS163		10.9.19	10.9.19			

EXPT	Experiment Planned		CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
							CC / II	Dean
E		Asynchronous Counters	2					
	E1	Design and Implement 4 bit MOD N and MOD NN ripple up/down counter using 74HC393		17.9.19	17.9.19			
F		8- Bit Universal Shift Register	2					
	F1	Verify different modes of Universal Shift Register		24.9.19	24.9.19			
	F2	Design and Implement MOD 8 Ring counter using IC 74F194		15.10.19	1.10.19			
G		Sequence detector	3					
	G1	Design and Implement 11011 sequence using IC 74LS 76		22.10.19	15.10.19			
H		Application implementation	3					
	H1	Design and Implement traffic Light Controller using suitable Flip flop (D FF- IC 74LS74)		29.10.19	29.10.19			
		Project presentation / Demonstration	All	5.11.19	5.11.19			
		Practical Examination	All	19.11.19	19.11.19			

TOTAL EXPT. PLANNED	12
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TOTAL EXPT. CONDUCTED	12
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Start of Term (Sign and Date)	
Course Instructor	
Class Coordinator / Instructor Incharge	
Dean	

End of Term (Sign and Date)	
Course Instructor	
Class Coordinator / Instructor Incharge	
Dean	

Verified by
Dean - Academics

MIT Academy of Engineering (An Autonomous Institute) Alandi (D), Pune - 412105 SCHOOL OF ELECTRICAL ENGG.	LAB COURSE RECORD		COURSE INSTRUCTOR	PRACHI RAJARAPOLLU
			COURSE	DSA
	ACADEMIC YEAR	2019-20	CLASS	SY BTECH
	SEM / TRI	III	DIVISION / BLOCK	B3, Batch II
			CYCLE	NA
			HRS / WEEK	2

EXPT	Experiment Planned		CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
							CC / II	Dean
A		Code Converter using IC 74HC154	1					
	A1	Design and Implement 4 bit Binary to Grey Code Converter vice versa		5.8.19	19.8.19			
	A2	Design and Implement multiple output logic function using IC 74HC154		19.8.19	19.8.19			
B		Parity Generators & Checkers	1					
	B1	Design and Implement 8,9 and 16 bit Even/Odd Parity Generator using IC 74LS180		26.8.19	26.8.19			
	B2	Design and Implement 8,9 and 16 bit Even/Odd Parity Checker using IC 74LS180		26.8.19	27.8.19			
C		Adders and Subtractors	1					
	C1	Design and Implement 1 digit BCD adder, subtractor using IC 74LS83		9.9.19	9.9.19			
D		Synchronous Counters	2					
	D1	Design and Implement MOD N and MOD NN Binary counters using IC 74LS163		16.9.19	16.9.19			
	D2	Design and Implement MOD N and MOD NN BCD counters using IC 74LS163		16.9.19	16.9.19			

EXPT	Experiment Planned		CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
							CC / II	Dean
E		Asynchronous Counters	2					
	E1	Design and Implement 4 bit MOD N and MOD NN ripple up/down counter using 74HC393		23.9.19	23.9.19			
F		8- Bit Universal Shift Register	2					
	F1	Verify different modes of Universal Shift Register		7.10.19	30.9.19			
	F2	Design and Implement MOD 8 Ring counter using IC 74F194		14.10.19	18.10.19			
G		Sequence detector	3					
	G1	Design and Implement 11011 sequence using IC 74LS 76		21.10.19	18.10.19			
H		Application implementation	3					
	H1	Design and Implement traffic Light Controller using suitable Flip flop (D FF- IC 74LS74)		4.11.19	19.10.19			
		Project presentation / Demonstration	All	11.11.19	4.11.19			
		Practical Examination	All	18.11.19	18.11.19			

TOTAL EXPT. PLANNED	12
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TOTAL EXPT. CONDUCTED	12
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Start of Term (Sign and Date)	
Course Instructor	<i>PPR</i> 1/11/19
Class Coordinator / Instructor Incharge	<i>AC</i> 18/11/19
Dean	<i>G</i> 20/11

End of Term (Sign and Date)	
Course Instructor	<i>PPR</i> 29/11/19
Class Coordinator / Instructor Incharge	<i>AC</i> 30/11/19
Dean	<i>G</i> 30/11



Verified by
Dean - Academics

<div><div>MIT</div><div>Academy of Engineering</div><div>(An Autonomous Institute)</div></div>	THEORY COURSE RECORD		COURSE INSTRUCTOR	MANDAR R. NALAVADE
			COURSE	ANALOG COMMUNICATION
Alandi (D), Pune - 412105	ACADEMIC YEAR	2019-20	CLASS	TY BTECH
SCHOOL OF ELECTRICAL ENGG.		SEM / TRI	V	DIVISION / BLOCK
	CYCLE			NA
			LECT / WEEK	3 Hrs

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - in case of variance)	Monitored by	
								CC / II	Dean
1	1.1	Electromagnetic Spectrum, Review of sig.	1	1	05-08-2019	07/08/2019	Lectures were not conducted on 05/08 due to heavy rain & non instructional day		
2	1.2	Representation of signals, Comm. Resources	2	1	05-08-2019	14/08/2019			
3	1.3	Communication resources, Signal trans.	3	1	07-08-2019	14/08/2019			
4	1.4	Block diagram representation of comm sys.	4	1	14-08-2019	19/08/2019			
5	1.5	Extra : Modulation, Demodulation concept	5, 6	1	14-08-2019	19/08/2019			
6		Guest Lecture			19-08-2019	21/08/2019			
7		Guest Lecture			19-08-2019	21/08/2019			
8	1.6	Classification & source of noise, SNR	7	1	21-08-2019	26/08/2019			
9	1.7	Extra : Noise parameters	8	1	21-08-2019	26/08/2019			
10	1.8	Math problems, Activity - Class Test		1	26-08-2019	28/08/2019			
11	2.1	Principles, types of amplitude modulation	9,10,11	2	26-08-2019	28/08/2019			

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by			
								CC / II	Dean		
12	2.2	AM current, power relation, Math analysis	12, 13	2	28-08-2019	04/09/2019		July	4		
13	2.3	Extra : Balanced modulator	14	2	28-08-2019	04/09/2019					
14	2.4	DSBSC: modulation, generation detection	14	2	04-09-2019	09/09/2019					
15	2.5	Extra : SSB, mod, generation, det.	14	2	04-09-2019	09/09/2019					
16	2.6	VSB : modulation, generation, detection	14	2	09-09-2019	11/09/2019					
17	2.7	Comparison of AM, DSBSC, SSB, VSB	14	2	09-09-2019	11/09/2019					
18	2.8	Activity - Quiz, Math Problems		2	11-09-2019	16/09/2019	class test is	July		4	
19	3.1	Extra : Principles, types of angle modulation	15, 16	2	11-09-2019	16/09/2019	conducted				
20	3.2	Important paramteres	17	2	16-09-2019	17/09/2019					
21	3.3	Mathematical analysis	18	2	16-09-2019	17/09/2019					
22	3.4	Narrow band and wide band FM	19	2	18-09-2019	18/09/2019					
23	3.5	Generation of FM wave	19	2	23-09-2019	23/09/2019					
24	3.6	Comparison of AM, FM and PM	21	2	30-09-2019	23/09/2019		July			4
25	3.7	Advantages, disadv. & appl.	20	2	30-09-2019	25/09/2019					
26	3.8	Activity - Discussion		2	07-10-2019	25/09/2019					
27	4.1	AM transmitters	22	3	07-10-2019	7/10/2019					
28	4.2	Receiver parameters	23	3	09-10-2019	7/10/2019					

Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
								CC / II	Dean
29	4.3	Extra : AM receivers	24	3	09-10-2019	9/10/2019		Juf	A
30	4.4	Double conversion AM receivers	24	3	14-10-2019	14/10/2019			
31	4.5	Linear integrated ckt AM rece.	24	3	14-10-2019	14/10/2019			
32	4.6	FM receiver	24	3	16-10-2019	16/10/2019			
33	4.7	Extra : FM stereo broadcasting	24, 25	3	16-10-2019	19/10/2019	Holiday on 21/10 due to election		
34		Extra (After College Hours) Beyond Syllabus : Rad			16-10-2019	19/10/2019		Juf	A
35	4.8	Two way FM radio comm.	24, 25	3	21-10-2019	23/10/2019			
36	5.1	Concept of random variables, proc., prob.	26	4	21-10-2019	3/11/2019			
37	5.2	Statistical independence, BT	27	4	23-10-2019	4/11/2019			
38	5.3	Extra : Prob. Density , distribution eg.	28	4	23-10-2019	6/11/2019			
39	5.4	Central limit theorem	29	4	04-11-2019	13/11/2019		Juf	A
40	5.5	Random process, significance	30	4	04-11-2019	18/11/2019			
41	5.6	Statistical averages	31	4	06-11-2019	18/11/2019			
42	5.7	Extra : Power spectral densities, transmission	31	4	06-11-2019	18/11/2019			
43	5.8	Activity - Group Problem Sol.		4	11-11-2019	20/11/2019			
44	6.1	Noise in communication system, types	32	5	11-11-2019	20/11/2019		Juf	A
45	6.2	Power spectral density of noise, problems	33	5	13-11-2019	20/11/2019			

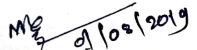
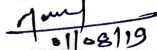
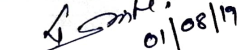
Lect. No.	Topics to be Delivered		TLO No.	CO No.	Date of Plan	Date of Conduction	Remark (mandatory - incase of variance)	Monitored by	
								CC / II	Dean
46	6.3	Extra : Noise calculations in DSBSC	34	5	13-11-2019	11/11/2019			
47	6.4	Noise in AM wave	34	5	18-11-2019	11/11/2019			
48	6.5	Noise in envelope detector, FM	34	5	18-11-2019				
49	6.6	Threshold effect in FM, pre-emphasis etc	34	5	20-11-2019				
50	6.7	Extra : Activity - QA Session	35	5	20-11-2019				

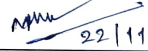
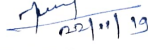

TOTAL LECTURES PLANNED

50

TOTAL LECTURES CONDUCTED

47

Start of Term (Sign and Date)	
Course Instructor	 01/08/2019
Class Coordinator / Instructor Incharge	 01/08/19
Dean	 01/08/19

End of Term (Sign and Date)	
Course Instructor	 22/11/2019
Class Coordinator / Instructor Incharge	 22/11/19
Dean	 22/11/19

Verified by
Dean - Academics

<div><div>MIT</div><div>Academy of Engineering</div><div>(An Autonomous Institute)</div></div>	LAB COURSE RECORD		COURSE INSTRUCTOR	MANDAR R. NALAVADE
			COURSE	ANALOG COMM.
Alandi (D), Pune - 412105	ACADEMIC YEAR	2019-20	CLASS	TY BTECH
SCHOOL OF ELECTRICAL ENGG.			DIVISION / BLOCK	II (Batch I)
	SEM / TRI	V	CYCLE	NA
HRS / WEEK			2 Hrs	

EXPT	Experiment Planned		CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
							CC / II	Dean
A	A1	AM modulator	2	02-08-2019	02/08/2019			
	A2	AM demodulator	2	09-08-2019	09/08/2019			
B	B1	DSBSC modulator	2	16-08-2019	16/08/2019			
C	C1	SSBSC modulator	2	23-08-2019	23/08/2019			
		Super Heterodyne Receiver : Beyond Content		30-08-2019	30/08/2019			
D	D1	Sampling	2	06-09-2019	06/09/2019			
E	E1	FM modem	2	13-09-2019	13/09/2019			
F	F1	AM transmitter	3,4	20-09-2019	20/09/2019			
G	G1	Circuit design of mixer	3	04-10-2019	27/09/2019	Exam is postponed		
	G2	Build using Multisim/MATLAB	3	11-10-2019	11/10/2019			
		Extra : Revision		17-10-2019	18/10/2019	Holiday on 25/10/2019		
H	H1	Circuit design of pre-emphasis and de-emphasis	3,5	18-10-2019	12/10/2019			
	H2	Build using Multisim/MATLAB	3,5	25-10-2019	8/11/2019			

EXPT	Experiment Planned		CO No.	Date of Plan	Date of Conduction	Remarks of Faculty (mandatory incase of variance)	Monitored By	
							CC / II	Dean
I	11	Circuit design of any of the above modulation tech.	2	15-11-2019	15/11/2019			
	12	Build using Multisim/MATLAB	2	22-11-2019	22/11/2019	Extra practical was on		
		Practical Exam		29-11-2019	22/11/2019	Revision		

TOTAL EXPT. PLANNED	16
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TOTAL EXPT. CONDUCTED	16
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Start of Term (Sign and Date)	
Course Instructor	M/S 01/08/19
Class Coordinator / Instructor Incharge	01/08/19
Dean	01/08/19

End of Term (Sign and Date)	
Course Instructor	M/S 22/11/19
Class Coordinator / Instructor Incharge	22/11/19
Dean	22/11/19

Verified by
Dean - Academics