

MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE										FY MTECH			
Alandi (D), Pune - 412 105		ACADEMIC YEAR : 2019 - 2020										DIVISION / BLOCK : NA			
SCHOOL OF ELECTRICAL ENGG.		TRI : II										CYCLE : NA			
		W.E.F. : 16-12-2019										THEORY : 08 HRS			
Time Day	8:30 to 9:25	9:26 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:16 to 14:10	14:10 to 15:05	15:06 to 15:10	15:10 to 16:00	16:00 to 16:50				
MON	1	2	3	4	5	6	7	8	9	10	11				
TUE	S H O R T B R E A K														
WED	L U N C H B R E A K														
THU	ACMOS SAP D301		S H O R T B R E A K												
FRI	ACMOS SAP D301		ACMOS SAP D301		AEA&P PSK D301		AEA&P PSK D301		AEA&P BATCH 1 (PSK) D301 (VLSI Lab)		AEA&P PSK D301		ACMOS BATCH 1 (SAP) D302 (Robotics Lab)		

Analog CMOS Design (ACMOS) Salyajit Pangaonkar (SAP)

Advanced Embedded Architecture & Processor (AEA&P) Prabha S. Kasliwal (PSK)

Research Methodology (RM) Shridhar Khandekar (SAK)

(Signature)
TIME TABLE INCHARGE

(Signature)
DEAN

DEAN - ACADEMICS

