

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120283001 ANDHALE POONAM PANDITRAO ALKA ,71310523C ,MAEA ,B120283001

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	28	46	P
404182	COMPUTER NETWORKS	PP	100	40	18	33	51	P C
404183	MICROWAVE ENGINEERING	PP	100	40	23	06	--	F
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	47	66	P C
404185C	PLCS AND AUTOMATION	PP	100	40	07	40	47	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			21	P C
404188	PROJECT PHASE I	OR	50	20			39	P C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	31	44	P C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	11	29\$	40	P C
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	34	49	P C
404192D	WIRELESS NETWORKS	PP	100	40	14	30	44	P C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			30	P C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P C
404195	PROJECT PHASE II	TW	100	40			85	P C
404195	PROJECT PHASE II	PR	50	20			41	P C

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

B120283002 ARNAB SARKAR SUSHOBHANA ,71536579H ,MAEA ,B120283002

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	28	42	P C
404182	COMPUTER NETWORKS	PP	100	40	20	30	50	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	34	52	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	41	55	P C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	15	35	50	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			32	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P C
404188	PROJECT PHASE I	OR	50	20			41	P C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	40	55	P C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	33	51	P C
404191A	SPEECH & AUDIO SIGNAL PROC.	PP	100	40	13	32	45	P C
404192D	WIRELESS NETWORKS	PP	100	40	12	37	49	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			28	P C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			22	P C
404195	PROJECT PHASE II	TW	100	40			64	P C
404195	PROJECT PHASE II	PR	50	20			36	P C

GRAND TOTAL = 846/1500 , RESULT:HIGHER SECOND CLASS

B120283003 JOGDAND SIDDHANT ASHOKRAO MANGAL ,71108764E ,MAEA ,B120283003

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	28	40	P C
404182	COMPUTER NETWORKS	PP	100	40	15	36	51	P C
404183	MICROWAVE ENGINEERING	PP	100	40	13	29	42	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	04	17	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	08	33	41	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			28	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			21	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			32	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P C
404188	PROJECT PHASE I	OR	50	20			33	P C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	37	47	P C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	11	29\$	40	P C
404191A	SPEECH & AUDIO SIGNAL PROC.	PP	100	40	12	30	42	P C
404192D	WIRELESS NETWORKS	PP	100	40	10	30\$	40	P C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			25	P C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			22	P C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			27	P C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			20	P C
404195	PROJECT PHASE II	TW	100	40			70	P C
404195	PROJECT PHASE II	PR	50	20			30	P C

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120283004 MATE RANI SHANTARAM SHALAN ,71410744B ,MAEA ,B120283004

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	28	40	P	C
404182	COMPUTER NETWORKS	PP	100	40	13	28	41	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	28	41	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	32	42	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	20	31	51	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			21	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	28	40	P	C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	28	46	P	C
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	29	42	P	C
404192D	WIRELESS NETWORKS	PP	100	40	18	41	59	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			30	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	C
404195	PROJECT PHASE II	TW	100	40			87	P	C
404195	PROJECT PHASE II	PR	50	20			41	P	C

GRAND TOTAL = 829/1500 , RESULT:HIGHER SECOND CLASS

B120283005 MEHAKARE ANKIT ARUN VARSHA ,71410749C ,MAEA ,B120283005

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	30	43	P	
404182	COMPUTER NETWORKS	PP	100	40	16	30	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	10	37	47	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	35	50	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	15	42	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	16	--	F	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	11	23	--	F	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	28	42	P	
404192D	WIRELESS NETWORKS	PP	100	40	18	31	49	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	C
404195	PROJECT PHASE II	TW	100	40			89	P	C
404195	PROJECT PHASE II	PR	50	20			43	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120283006 PAWAR HARSHALA POPAT KALPANA ,71209681H ,MAEA ,B120283006

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	40	49	P	
404182	COMPUTER NETWORKS	PP	100	40	15	36	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	49	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	38	52	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	15	31	46	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			21	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	09	22*	31*	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	34	51	P	C
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	42	54	P	C
404192D	WIRELESS NETWORKS	PP	100	40	20	33	53	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P	C
404195	PROJECT PHASE II	TW	100	40			87	P	C
404195	PROJECT PHASE II	PR	50	20			42	P	C

GRAND TOTAL = 903/1500 , RESULT:FIRST CLASS \*[0.140A+0.4]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120283007 SHIVAM RAAJ ANURADHA ,71537022H ,MAEA ,B120283007

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	30	45	P	C	404189	MOBILE COMMUNICATION	PP	100	40	12	40	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	51	71	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	34	58	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	29	42	P	C	404191A	SPEECH & AUDIO SIGNAL PROC.	PP	100	40	10	30\$	40	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	35	54	P	C	404192D	WIRELESS NETWORKS	PP	100	40	19	46	65	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	44	62	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C	404195	PROJECT PHASE II	TW	100	40			65	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			35	P	C

GRAND TOTAL = 914/1500 , RESULT:FIRST CLASS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

B120283008 WAGHMODE ROHIT MACHHINDRA INDUMATI ,71209817J ,MAEA ,B120283008

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	AA	--	F		404189	MOBILE COMMUNICATION	PP	100	40	04	AA	--	F	
404182	COMPUTER NETWORKS	PP	100	40	12	43	55	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	28	41	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	08	AA	--	F		404191A	SPEECH & AUDIO SIGNAL PROC.	PP	100	40	12	28	40	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	33	45	P	C	404192B	NANO ELECTRONICS & MEMS	PP	100	40	08	AA	--	F	
404185C	PLCS AND AUTOMATION	PP	100	40	16	31	47	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			32	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			20	P	C	404195	PROJECT PHASE II	TW	100	40			73	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C	404195	PROJECT PHASE II	PR	50	20			37	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS

B120283009 PARMAR HARDIKSINH VIKRAMSINH URMILABEN ,71236771D ,MAEA ,B120283009

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	33	43	P	C	404189	MOBILE COMMUNICATION	PP	100	40	11	29\$	40	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	AA	--	F		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	AA	--	F	
404183	MICROWAVE ENGINEERING	PP	100	40	08	AA	--	F		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	32	52	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	31	43	P	C	404192B	NANO ELECTRONICS & MEMS	PP	100	40	11	38	49	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	14	29	43	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			32	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C	404195	PROJECT PHASE II	TW	100	40			70	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C	404195	PROJECT PHASE II	PR	50	20			35	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120283020 MD. WARIS AHMAD NAZ PERWEEN ,71053089H ,MAEA ,B120283020

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	30\$	40	P
404182	COMPUTER NETWORKS	PP	100	40	14	28	42	P C
404183	MICROWAVE ENGINEERING	PP	100	40	11	29\$	40	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	05	45	50	P C
404185C	PLCS AND AUTOMATION	PP	100	40	04	38	42	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			28	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			21	P C
404188	PROJECT PHASE I	OR	50	20			33	P C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	42	57	P C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	36	51	P C
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	08	39	47	P C
404192B	NANO ELECTRONICS & MEMS	PP	100	40	16	33	49	P C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			28	P C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P C
404195	PROJECT PHASE II	TW	100	40			70	P C
404195	PROJECT PHASE II	PR	50	20			35	P C

GRAND TOTAL = 809/1500 , RESULT:SECOND CLASS [\$ 0.1]