

MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE										CLASS	SY BTECH (E&TC)
Alandi (D), Pune - 412 105 <td colspan="10">ACADEMIC YEAR : 2019 - 2020 <td>DIVISION / BLOCK</td> <td>A</td> </td>		ACADEMIC YEAR : 2019 - 2020 <td>DIVISION / BLOCK</td> <td>A</td>										DIVISION / BLOCK	A
SCHOOL OF ELECTRICAL ENGG. <td colspan="10">SEM/TRI : IV <td>CYCLE</td> <td>NA</td> </td>		SEM/TRI : IV <td>CYCLE</td> <td>NA</td>										CYCLE	NA
		W.E.F. : 06/01/2020 <td>THEORY</td> <td>19 HRS</td>										THEORY	19 HRS
												PRACTICAL	24 HRS
												TUTORIAL	NIL
												TOTAL	43 HRS
Time	8:30 to 9:25	9:25 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:15 to 14:10	14.10 to 15:05	15.05 to 15:10	15:10 to 16.00	16:00 to 16:50		
Day	1	2	3	4	5	6	7	8	9	10	11		
MON	A1-SS-SSK-PEL-A013 A2-DSA-SDD-SPL-A103 A3-AM-ADC-ICL-A003 A4-SE-AAD-SSL-D306			SS SSK A112	NAT ADG A112		AM ADC A113	SE VBK A113		A3-NAT-ADG-ICL-A003			
TUE	A1-SE-AAD-SSL-D306 A2-SS-SSK-PEL-A013 A3-DSA-SDD-SPL-A103 A4-AM-ADC-ICL-A003			ENVIRONMENTAL SCIENCE VDP BATCH 1 & 2 (D305)		L U N C H B R E A K	SE VBK D308	SE VBK D308		A4-NAT-ADG-ICL-A003			
WED	A1-AM-ADC-ICL-A003 A2-SE-VBK-SSL-D306 A3-SS-SSK-PEL-A013 A4-DSA-SDD-SPL-A103			SS SSK A113	NAT ADG A113		ES VDP A113	NAT ADG A113		AM ADC A112	AM ADC A112		
THU	A1-DSA-SDD-SPL-A103 A2-AM-ADC-ICL-A003 A3-SE-VBK-SSL-D306 A4-SS-SSK-PEL-A013			ENVIRONMENTAL SCIENCE VDP BATCH 3 & 4 (D305)			PROFESSIONAL SKILL AVN A106	PROFESSIONAL SKILL AVN A106		DEPARTMENT MEETING			
FRI	A1-NAT-ADG-ICL-A003			PROJECT SAP D305	PROJECT SAP D305		SS SSK A112	ES VDP A112		A2-NAT-ADG-ICL-A003			

SIGNAL & SYSTEM (SS) SMITA KULKARNI (SSK)
 NETWORK ANALYSIS TECH. (NAT) DR. ANIKET GUNDECHA (ADG)
 PROJECT SATYAJIT PANGAONKAR (SAP)
 APPLIED MATHEMATICS (AM) ASHITOSH CHAVAN (ADC)
 DATA STRUCTURES & ALGORITHMS (DSA) SWAPNIL DAPHAL (SDD)

TIME TABLE INCHARGE

PROFESSIONAL SKILL (PS) AMIT NAGARALE (AVN)
 EXTERNAL FACULTY
 SYSTEM ENGINEERING (SE) VINAYAK KULKARNI (VBK)
 ENVIRONMENTAL SCIENCE (ES) ANJALI DESHPANDE (AAD)
 VINOD PAKHLE (VDP)

PEL POWER ELECTRONIS LAB
 SPL SIGNAL PROCESSING LAB
 ICL INSTRU & CONTROL LAB
 ACL ADVANCED COMM. LAB

DEAN - ACADEMICS

MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE										CLASS	SY BTECH (E&TC)
Alandi (D), Pune - 412 105 <td colspan="2">ACADEMIC YEAR</td> <td>:</td> <td colspan="2">2019 - 2020</td> <td colspan="2">DIVISION / BLOCK</td> <td colspan="2">B</td> <td colspan="2">CYCLE</td> <td>NA</td>		ACADEMIC YEAR		:	2019 - 2020		DIVISION / BLOCK		B		CYCLE		NA
SCHOOL OF ELECTRICAL ENGG. <td colspan="2">SEM/TRI</td> <td>:</td> <td colspan="2">IV</td> <td colspan="2">THEORY</td> <td colspan="2">18 HRS</td> <td colspan="2">PRACTICAL</td> <td>24 HRS</td>		SEM/TRI		:	IV		THEORY		18 HRS		PRACTICAL		24 HRS
<td colspan="2">W.E.F.</td> <td>:</td> <td colspan="2">06/01/2020</td> <td colspan="2">TUTORIAL</td> <td colspan="2">NIL</td> <td colspan="2">TOTAL</td> <td>42 HRS</td>		W.E.F.		:	06/01/2020		TUTORIAL		NIL		TOTAL		42 HRS
Time	8:30 to 9:25	9:25 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:15 to 14:10	14:10 to 15:05	15:05 to 15:10	15:10 to 16:00	16:00 to 16:50		
Day	1	2	3	4	5	6	7	8	9	10	11		
MON	PROJECT SAP D305	PROJECT SAP D305	S H O R T B R E A K	PROFESSIONAL SKILL AVN A113	PROFESSIONAL SKILL AVN A113	L U N C H B R E A K	B1-NAT-PAK-ICL-A003		S H O R T B R E A K	B1-SS-SAP-PEL-A013 B2-DSA-SDD-SPL-A103 B3-AM-ADC-SSL-D306 B4-SE-VBK-VLSI-D301			
TUE	NAT PKA A113	ES MDS A113		AM HRL A113	SS SSK A113		PROFESSIONAL SKILL BATCH 3 & 4 (D307) EXTERNAL			B1-SE-VBK-VLSI-D301 B2-SS-SAP-PEL-A013 B3-DSA-SDD-SPL-A103 B4-AM-ADC-SSL-D306			
WED	NAT PKA A113	ES MDS A113		NAT PKA A105	SS SSK A105		SE VBK D305	SE VBK D305		B1-AM-HRL-ICL-A003 B2-SE-VBK-SSL-D306 B3-SS-SAP-PEL-A013 B4-DSA-SDD-SPL-A103			
THU	AM HRL A113	AM HRL A113		B4-NAT-PAK-ICL-A003 B1 & B2 -ES-MDS-ICL-ICA A210			ENVIRONMENTAL SCIENCE MDS BATCH 3 & 4 (A112)			DEPARTMENT MEETING			
FRI	SE VBK A113	SS SSK A113		B2-NAT-PAK-ICL-A003			PROFESSIONAL SKILL BATCH 1 & 2 (A113) EXTERNAL			B1-DSA-SDD-SPL-A103 B2-AM-ADC-SSL-D306 B3-SE-AAD-VLSI-D301 B4-SS-SSK-PEL-A013			
							B3-NAT-PAK-ICL-A003						

SIGNAL & SYSTEM (SS)

SMITA KULKARNI (SSK)
SATYAJIT PANGAONKAR (SAP)
PRASHANT AHER (PKA)
SATYAJIT PANGAONKAR (SAP)
HITESH LODHA (HRL)
ASHITOSH CHAVAN (ADC)

NETWORK ANALYSIS TECH. (NAT)
PROJECT

APPLIED MATHEMATICS (AM)

TIME TABLE INCHARGE

PROFESSIONAL SKILL (PS)
SYSTEM ENGINEERING (SE)
ENVIRONMENTAL SCIENCE (ES)
DATA STRUCTURES & ALGORITHMS (DSA)

AMIT NAGARALE (AVN)
EXTERNAL FACULTY
VINAYAK KULKARNI (VBK)
Dr. PRACHI RAJARAPOLLU (PRR)
M. D. SARDARE (MDS)
SWAPNIL DAPHAL (SDD)

PEL POWER ELECTRONIS LAB
SPL SIGNAL PROCESSING LAB
ICL INSTRU & CONTROL LAB
ACL ADVANCED COMM. LAB

DEAN ACADEMICS

MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE										CLASS	TY BTECH (E&TC)
Alandi (D), Pune - 412 105 <td colspan="2">ACADEMIC YEAR</td> <td colspan="2">:</td> <td colspan="2">2019 - 2020</td> <td colspan="2">PRACTICAL</td> <td colspan="2">18 HRS <td>DIVISION / BLOCK</td> <td>A</td> </td>		ACADEMIC YEAR		:		2019 - 2020		PRACTICAL		18 HRS <td>DIVISION / BLOCK</td> <td>A</td>		DIVISION / BLOCK	A
SCHOOL OF ELECTRICAL ENGG. <td colspan="2">SEM/TRI</td> <td colspan="2">:</td> <td colspan="2">VI</td> <td colspan="2">TUTORIAL</td> <td colspan="2">NIL <td>CYCLE</td> <td>NA</td> </td>		SEM/TRI		:		VI		TUTORIAL		NIL <td>CYCLE</td> <td>NA</td>		CYCLE	NA
		W.E.F.		:		06/01/2020		TOTAL		34 HRS <td>THEORY</td> <td>16 HRS </td>		THEORY	16 HRS
Time	8:30 to 9:25	9:25 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:15 to 14:10	14:10 to 15:05	15:05 to 15:10	15:10 to 16:00	16:00 to 16:50		
Day	1	2	3	4	5	6	7	8	9	10	11		
TUE	DC DA A105	DC DA A105	S H O R T B R E A K	A3 - ATD - AKS - ACL - A110		L U N C H B R E A K	LIBRARY HOURS	DSP NVB A113	S H O R T B R E A K	IOT / EP / K&D SSN / MCV / YLM BATCH 1 A210 / A109 / D302			
WED	DC DA A105	IOT / EP / K&D SSN / MCV / BRP A106/A105/A112		A1 - PS - EXT - D307 A2 - DC - AKK - EDC - A201 A3 - DSP - NVB - RL - D302 A4 - ATD - AKS - ACL - A110			PROJECT PKA A106	DSP NVB A106		IOT / EP / K&D SSN / MCV / BRP BATCH 2 A201 / A109 / D302			
THU	IOT / EP / K&D SSN / MCV / BRP A106/A105/A112	IOT / EP / K&D SSN / MCV / BRP A106/A105/A112		A1 - ATD - AKS - ACL - A110 A3 - DC - AKK - EDC - A201 A4 - DSP - NVB - RL - D302			DSP NVB A105	ATD AKS A105		PROFESSIONAL SKILL AKS A105	PROFESSIONAL SKILL AKS A105		
FRI	IOT / EP SSN / MCV BATCH 3 A201 / A109			A1 - DSP - NVB - RL - D302 A2 - ATD - AKS - ACL - A110 A3 - PS - EXT - A106 A4 - DC - MRN - EDC - A201			ATD AKS A105	ATD AKS A105		IOT / EP SSN / MCV BATCH 4 A201 / A109			
SAT	A2 - PS - EXT - A112			A1 - DC - AKK - EDC - A201 A2 - DSP - NVB - RL - D302 A4 - PS - EXT - A112			PROJECT PKA A113						

DIGITAL COMMUNICATION (DC) DR. DEBASHIS ADHIKARI (DA)
ANIKET KEMALKAR (AKK)
DIGITAL SIGNAL PROCESSING (DSP) MANDAR NALVADE (MRN)
ANTENNA THEORY DESIGN (ATD) NUTAN V BANSODE (NVB)
PROFESSIONAL SKILL (PS) ASHISH SRIVASTAVA (AKS)
PROJECT ASHISH SRIVASTAVA (AKS)
PRASHANT K AHER (PKA)

TIME TABLE INCHARGE

IoT NETWORKS & PROTOCOL (IOT) SANDEEP S NAGRE (SSN)
OPEN ELECTIVE (OE) EMBEDDED PROCESSOR (EP) MAHESH C VIBHUTE (MCV)
KINEMATICS & DYNAMICS (K&D) B. R. PATIL (BRP)
Y. L. MASKE YLM

DEAN

EDC ELECTRONIC DEVICE LAB
RL ROBOTICS LAB
ACL ADVANCED COMMUNICATION LAB
ESL EMBEDDED SYSTEM LAB

DEAN - ACADEMICS

MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE									
Alandi (D), Pune - 412 105		ACADEMIC YEAR		1	2019 - 2020		CLASS		TY ITECH (E&TC)		
SCHOOL OF ELECTRICAL ENGG.		SEM/TRI		1	VI		DIVISION / BLOCK		B		
		W.E.F.		1	06/01/2020		CYCLE		NA		
							THEORY		16 HRS		
							PRACTICAL		18 HRS		
							TUTORIAL		NIL		
							TOTAL		34 HRS		
Time Day	8:30 to 9:25	9:25 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:15 to 14:10	14:10 to 15:05	15:05 to 15:10	15:10 to 16:00	16:00 to 16:50
	1	2	3	4	5	6	7	8	9	10	11
TUE	DSP NVB A108	ATD AKK A108	S H O R T B R E A K	PROJECT PKA A112	PROJECT PKA A112	L U N C H B R E A K	PROFESSIONAL SKILL AKB EDG A201	PROFESSIONAL SKILL AKB EDG A201	S H O R T B R E A K	IOT / EP / K&D SSN / MCV / YLM BATCH 1 A201 / A109 / D302	
WED	DSP NVB A108	IOT / EP / K&D SSN / MCV / BRP A108/A109/A112		LIBRARY HOURS	LIBRARY HOURS		B1 - PB - EXT - D307 B2 - DG - MRN - EDO - A201 B3 - DSP - BKR - RL - D302 B4 - ATD - AKK - ACL - A110	IOT / EP / K&D SSN / MCV / BRP BATCH 2 A201 / A109 / D302			
THU	IOT / EP / K&D SSN / MCV / BRP A108/A109/A112	IOT / EP / K&D SSN / MCV / BRP A108/A109/A112		DO MRN A113	DO MRN A113		B1 - ATD - AKK - ACL - A110 B3 - DG - MRN - EDO - A201 B4 - DSP - BKR - RL - D302	B2 - PB - EXT - A108			
FRI	IOT / EP SSN / MCV BATCH 3 A201 / A109			ATD AKK A108	ATD AKK A108		B1 - DSP - NVB - RL - D302 B2 - ATD - AKK - ACL - A110 B3 - PB - EXT - D307 B4 - DG - MRN - EDO - A201	IOT / EP SSN / MCV BATCH 4 A201 / A109			
SAT		DSP NVB A108		DO MRN A108	LIBRARY HOURS		B1 - DG - MRN - EDO - A201 B2 - DSP - BKR - RL - D302 B3 - ATD - AKK - ACL - A110 B4 - PB - EXT - A108				

DIGITAL COMMUNICATION (DC) MANDAR NALAVADE (MRN)
 DIGITAL SIGNAL PROCESSING (DSP) NUTAN V BANBODE (NVB)
 ANTENNA THEORY DESIGN (ATD) BHILPA RUDRAWAR (BKR)
 PROJECT ANIKET KEMALKAR (AKK)
 PROFESSIONAL SKILL (PS) PRABHANT K AJIER (PKA)
 EXTERNAL FACULTY ASHISHI BRIVABTAVA (AKB)

TIME TABLE INCHARGE

IOT NETWORKS & PROTOCOL (IOT) BANDEEP B NAGRE (BBN)
 OPEN ELECTIVE (OE) EMBEDDED PROCESSOR (EP) MAHESH G VIBHUTE (MGV)
 KINEMATICS & DYNAMICS (K&D) B. R. PATIL (BRP)
 Y. L. MABRE YLM



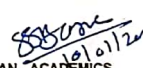
DEAN

EDO ELECTRONIC DEVICE LAB
 RL ROBOTICS LAB
 ACL ADVANCED COMMUNICATION LAB
 EMBEDEDDED SYSTEM LAB

DEAN - ACADEMICS

MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE										CLASS	BTECH E&TC		
Alandi (D), Pune - 412 105 <td colspan="10">ACADEMIC YEAR</td> <td>:</td> <td>2019 - 2020</td> <td>DIVISION / BLOCK</td> <td>A</td>		ACADEMIC YEAR										:	2019 - 2020	DIVISION / BLOCK	A
SCHOOL OF ELECTRICAL ENGG. <td colspan="10">SEM/TRI</td> <td>:</td> <td>VIII</td> <td>CYCLE</td> <td>IIA</td>		SEM/TRI										:	VIII	CYCLE	IIA
		W.E.F.										:	06/01/2020	THEORY	13 HRS
														PRACTICAL	16 HRS
														TUTORIAL	NIL
														TOTAL	29 HRS
Time	8:30 to 9:25	9:25 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:15 to 14:10	14:10 to 15:05	15:05 to 15:10	15:10 to 16:00	15:00 to 15:50				
Day	1	2	3	4	5	6	7	8	9	10	11				
MON	EMIOT/ RTES/ I&HPR VAK / AVN / SAK BATCH 1 A202 / A109 / D302		S H O R T B R E A K	ENGINEERING ECONOMICS D203	ENGINEERING ECONOMICS D203	L U N C H B R E A K	BE / AI / SSP UYV / RPB / SAP A105/A106/A112	BE / AI / SSP UYV / RPB / SAP A105/A106/A112	S H O R T B R E A K	ACS BATCH 2 (UPM) CL-A202					
TUE	EMIOT/ RTES/ I&HPR VAK / AVN / RPB BATCH 2 A202 / A109 / D302			ACS DA A105	ACS DA A105		EMIOT / RTES / I&HPR VAK / AVN / RPB A105/A106/A112	EMIOT / RTES / I&HPR VAK / AVN / RPB A105/A106/A112		ACS BATCH 1 (DA) CL-A202					
WED	EMIOT/ RTES/ I&HPR VAK / AVN / RPB BATCH 3 A202 / A109 / D302			ACS BATCH 3 (UPM) CL-A202			ACS DA A105								
THU	EMIOT/ RTES/ I&HPR VAK / AVN / SAK BATCH 4 A202 / A109 / D302			EMIOT / RTES / I&HPR VAK / AVN / RPB A105/A106/A112	BE / AI / SSP UYV / RPB / SAP A105/A106/A112		ACS BATCH 4 (UPM) CL-A202			DEPARTMENT MEETING					
FRI	PROJECT DYS A105	PROJECT DYS A105		PROJECT			PROJECT			PROJECT					

Advanced Comm. System (ACS)	DR. DEBASHIS ADHIKARI (DA)	Real Time Embedded System (RTES)	AMIT NAGARALE (AVN)	EDC	ELECTRONICS DEV LAB
	UDAY MITHAPELJI (UPM)	Energy Management for IoT (EMIOT)	VAISHALI A. KATKAR (VAK)	CL	COMMUNICATION LAB
Engineering Economics Project	ANUSHI TIWARI (AT)	OPEN ELECTIVE (OE)	DR. RUSHIKESH P. BORSE (RPB)	ACL	ADVANCED COMM. LAB
	DR. DIPTI Y. SAKHARE (DYS)	Intelligent & High Performance Robotics (I&HPR)	SHRIDHAR KHANDEKAR (SAK)	RL	ROBOTICS LAB
		DEPARTMENTAL ELECTIVE (DE)	USHA VERMA (UYV)		
		Biomedical Engineering (BE)	DR. RUSHIKESH P. BORSE (RPB)		
		Artificial Intelligence (AI)	SATYAJIT PANGAONKAR (SAP)		
		Speech Signal Processing (SSP)			

 TIME TABLE INCHARGE
 DEAN
 DEAN - ACADEMICS

MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE										CLASS	BTECH E&TC			
												DIVISION / BLOCK	B			
												CYCLE	IIA			
												THEORY	13 HRS			
												PRACTICAL	16 HRS			
												TUTORIAL	NIL			
												TOTAL	29 HRS			
Alandi (D), Pune - 412 105		ACADEMIC YEAR		:	2019 - 2020		SEM/TRI		:	VIII		W.E.F.		:	06/01/2020	
SCHOOL OF ELECTRICAL ENGG.				:					:					:		
Time	Day	8:30 to 9:25	9:25 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:15 to 14:10	14:10 to 15:05	15:05 to 15:10	15:10 to 15:50	16:00 to 16:50				
		1	2	3	4	5	6	7	8	9	10	11				
MON		EMIOT/ RTES/ I&HPR VAK / AVH / SAK BATCH 1 A202 / A109 / D302		S H O R T B R E A K	ACS BATCH 1 (SSH) CL-A202		L U N C H B R E A K	BE / AI / SSP UYV / RPB / SAP A105/A106/A112	BE / AI / SSP UYV / RPB / SAP A105/A106/A112	S H O R T B R E A K	ENGINEERING ECONOMICS D203	ENGINEERING ECONOMICS D203				
TUE		EMIOT/ RTES/ I&HPR VAK / AVH / RPB BATCH 2 A202 / A109 / D302			ACS MRH A106	ACS MRH A106		EMIOT / RTES / I&HPR VAK / AVH / RPB A105/A106/A112	EMIOT / RTES / I&HPR VAK / AVH / RPB A105/A106/A112		ACS BATCH 3 (UPM) EDC-A201					
WED		EMIOT/ RTES/ I&HPR VAK / AVH / RPB BATCH 3 A202 / A109 / D302			ACS MRH D205	LIBRARY HOURS		ACS BATCH 2 (SSH) CL-A202			ACS BATCH 4 (UPM) CL-A202					
THU		EMIOT/ RTES/ I&HPR VAK / AVH / SAK BATCH 4 A202 / A109 / D302			EMIOT / RTES / I&HPR VAK / AVH / RPB A105/A106/A112	BE / AI / SSP UYV / RPB / SAP A105/A106/A112					DEPARTMENT MEETING					
FRI		PROJECT UYV A106	PROJECT UYV A106		PROJECT			PROJECT			PROJECT					

Advanced Comm. System (ACS)
Engineering Economics
Project

MAHAR HANAYADE (MRH)
UDAY MITHAPPELLI (UPM)
SANDEEP NAGRE (SSH)
ANUSHI THAPRI (AT)
DR. DIPTI Y. SAKHARE (DYS)

OPEN ELECTIVE (OE)
DEPARTMENTAL
ELECTIVE (DE)

Real Time Embedded System (RTES)
Energy Management for IoT (EMIoT)
Intelligent & High Performance Robotics (I&HPR)
Biomedical Engineering (BE)
Artificial Intelligence (AI)
Speech Signal Processing (SSP)

AMIT NAGARALE (AVN)
VAISHALI A. KATKAR (VAK)
DR. RUSHIKESH P. BORSE (RPB)
SHRIDHAR KHANDEKAR (SAK)
USHA VERMA (UYV)
DR. RUSHIKESH P. BORSE (RPB)
BATYAJIT PANDEKAR (BAP)

EDC ELECTRONICS DEVICE LAB
ESL EMBEDDED SYSTEM LAB
CL COMMUNICATION LAB
ACL ADVANCED COMM. LAB
RL ROBOTICS LAB

TIME TABLE INCHARGE

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DEAN - ACADEMICS