

MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE										CLASS	SY BTECH (ETX)		
												DIVISION / BLOCK	C		
Alandl (D), Pune - 412 105		ACADEMIC YEAR										:	2019 - 2020	THEORY	16 HRS
SCHOOL OF ELECTRICAL ENGG.		SEM/TRI										:	IV	PRACTICAL	20 HRS
		W.E.F.										:	06/01/2020	TUTORIAL	NIL
												:		TOTAL	36 HRS
Time Day	8:30 to 9:25	9:25 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:15 to 14:10	14:10 to 15:05	15:05 to 15:10	15:10 to 16:00	16:00 to 16:50				
	1	2	3	4	5	6	7	8	9	10	11				
MON	SE VBK A113	SE VBK A113	S H O R T B R E A K	C1-CT-UYV-ICL-A003 C2-AM-HRL-SSL-D306		L U N C H B R E A K	PROFESSIONAL SKILL ALL BATCCHES D305 EXTERNAL			ES KK A105	ES KK A105				
TUE	SE VBK D305	AM HRL D305		C2-CT-UYV-ICL-A003			C1-SS-DYS-SPL-A103 C3-SE-AAD-SSL-D306			SS DYS A105	CT VAK A105				
WED	AM HRL D305	AM HRL D305		C3-CT-UYV-ICL-A003			C1-SE-AAD-SSL-D306 C2-SS-DYS-SPL-A103 C3-DSA-SAK-PEL-A013			PROFESSIONAL SKILL AVN A106	PROFESSIONAL SKILL AVN A106				
THU	SS DYS D305	SS DYS D305		PROJECT SAP D308	CT VAK D308		C1-DSA-SAK-PEL-A013 C2-SE-AAD-SSL-D306 C3-SS-DYS-SPL-A103								
FRI	C3-AM-HRL-SSL-D306 C2-DSA-SAK-PEL-A013			C1-AM-HRL-SSL-D306 C2 & C3 -ES-NVB-A112			CT VAK A113	PROJECT SAP A113	ENVIRONMENTAL SCIENCE C1 BATCH A105 NVB						

SIGNAL & SYSTEM (SS)  
CIRCUIT THEORY (CT)

DR. DIPTI SAKHARE (DYS)  
VAISHALI KATKAR (VAK)

USHA VERMA (UYV)

PROJECT  
APPLIED MATHEMATICS (AM)  
SATYAJIT PANGAONKAR (SAP)  
HITESH LODHA (HRL)

TIME TABLE INCHARGE

PROFESSIONAL SKILL (PS)

SYSTEM ENGINEERING (SE)

ENVIRONMENTAL SCIENCE (ES)

DATA STRUCTURES &  
ALGORITHMS (DSA)

AMIT NAGARALE (AVN)

EXTERNAL FACULTY

VINAYAK KULKARNI (VBK)

ANJALI DESHPANDE (AAD)

KHUSHAL KANADE (KK)

NIHKIL BHALEROA (NVB)

SHRIDHAR KHANDEKAR (SAK)

PEL POWER ELECTRONIS LAB

SPL SIGNAL PROCESSING LAB

VLSI VLSI LAB

HEL HYBRIDE ELE. LAB

DEAN - ACADEMICS

MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE										CLASS	TY BTECH (ETX)	
Alandi (D), Pune - 412 105 <td colspan="2">ACADEMIC YEAR</td> <td>:</td> <td colspan="2">2019 - 2020</td> <td colspan="2">DIVISION / BLOCK</td> <td colspan="3">C</td> <td colspan="2">CYCLE</td> <td>NA</td>		ACADEMIC YEAR		:	2019 - 2020		DIVISION / BLOCK		C			CYCLE		NA
SCHOOL OF ELECTRICAL ENGG. <td colspan="2">SEM/TRI</td> <td>:</td> <td colspan="2">VI</td> <td colspan="2">THEORY</td> <td colspan="3">16 HRS</td> <td colspan="2">PRACTICAL</td> <td>16 HRS</td>		SEM/TRI		:	VI		THEORY		16 HRS			PRACTICAL		16 HRS
		W.E.F.		:	06/01/2020		TUTORIAL		NIL			TOTAL		32 HRS
Time Day	8:30 to 9:25	9:25 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:15 to 14:10	14:10 to 15:05	15:05 to 15:10	15:10 to 16:00	16:00 to 16:50			
	1	2	3	4	5	6	7	8	9	10	11			
TUE	RTOS PSK A112	RTOS PSK A112	S H O R T  B R E A K	C1-RTOS - SRP - A109 C2-DSP - SKR - A103	L U N C H  B R E A K		PROJECT SKR VLSI D301	PROJECT SKR VLSI D301		IOT / EP / K&D SSN / MCV / YLM BATCH 1 A201 / A109 / D302				
WED	RTOS PSK A112	IOT / EP / K&D SSN / MCV / BRP A106/A105/A112		C1-PS - EXT - D308 C2-RTOS - SRP - A109			PROFESSIONAL SKILL AKS A112	PROFESSIONAL SKILL AKS A112		IOT / EP / K&D SSN / MCV / BRP BATCH 2 A201 / A109 / D302				
THU	IOT / EP / K&D SSN / MCV / BRP A106/A105/A112	IOT / EP / K&D SSN / MCV / BRP A106/A105/A112		C1-PE - NBS - A012 C2-PS - EXT - A107			DEPARTMENT MEETING							
FRI	IOT / EP SSN / MCV BATCH 3 A201 / A109			C1 - DSP- SKR - A103 C2- PE - NBS - A012			DSP SKR A106	PEA NBS A106		IOT / EP SSN / MCV BATCH 4 A201 / A109				
SAT	PEA NBS D305	PEA NBS D305		DSP SKR D305			DSP SKR D305							

POWER ELECTRONICS (PE) NIKHIL B SARDAR (NBS)  
 REAL TIME OPERATING SYSTEM (RTOS) PRABHA KASLIWAL (PSK)  
 DIGITAL SIGNAL PROCESSING (DSP) SAVITA PAWAR (SRP)  
 PROJECT SHILPA RUDRAWAR (SKR)  
 PROFESSIONAL SKILL (PS) PRASHANT KAHER (PKA)  
 EXTERNAL FACULTY ASHISH SRIVASTAVA (AKS)

IoT NETWORKS & PROTOCOL (IOT) SANDEEP S NAGRE (SSN)  
 OPEN ELECTIVE (OE) EMBEDDED PROCESSOR (EP) MAHESH C VIBHUTE (MCV)  
 KINEMATICS & DYNAMICS (K&D) B. R. PATIL (BRP)  
 Y L MASKE (YLM)

EDC ELCTRONIC DEVICE LAB  
 RL ROBOTICS LAB  
 ACLADVANCED COMMUNICATION LAB  
 ESL EMBEDDED SYSTEM LAB

TIME TABLE INCHARGE

DEAN

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MIT Academy of Engineering (An Autonomous Institute)		CLASS WISE TIME TABLE									
Alandi (D), Pune - 412 105 <td colspan="2">ACADEMIC YEAR</td> <td colspan="2">: 2019 - 2020</td> <td colspan="2">CLASS</td> <td colspan="2">BTECH ETX</td>		ACADEMIC YEAR		: 2019 - 2020		CLASS		BTECH ETX			
SCHOOL OF ELECTRICAL ENGG. <td colspan="2">SEM/TRI</td> <td colspan="2">: VIII</td> <td colspan="2">DIVISION / BLOCK</td> <td colspan="2">C</td>		SEM/TRI		: VIII		DIVISION / BLOCK		C			
		W.E.F.		: 06/01/2020		CYCLE		NA			
						THEORY		13 HRS			
						PRACTICAL		16 HRS			
						TUTORIAL		NIL			
						TOTAL		29 HRS			
Time Day	8:30 to 9:25	9:25 to 10:20	10:20 to 10:30	10:30 to 11:25	11:25 to 12:20	12:20 to 13:15	13:15 to 14:10	14:10 to 15:05	15:05 to 15:10	15:10 to 16:00	16:00 to 16:50
	1	2	3	4	5	6	8	9	10	11	
MON	EMIOT/ RTES/ I&HPR VAK / AVN / SAK BATCH 1 A202 / A109 / D302		S H O R T  B R E A K	CE ABC A106	CE ABC A106	L U N C H  B R E A K	BE / AI / SSP UYV / RPB / SAP A105/A106/A112	BE / AI / SSP UYV / RPB / SAP A105/A106/A112	S H O R T  B R E A K	CE BATCH 3 (ABC) PL-A107	
TUE	EMIOT/ RTES/ I&HPR VAK / AVN / RPB BATCH 2 A202 / A109 / D302			ENGINEERING ECONOMICS D203	ENGINEERING ECONOMICS D203		EMIOT / RTES / I&HPR VAK / AVN / RPB A105/A106/A112	EMIOT / RTES / I&HPR VAK / AVN / RPB A105/A106/A112		CE BATCH 4 (ABC) PL-A107	
WED	EMIOT/ RTES/ I&HPR VAK / AVN / RPB BATCH 3 A202 / A109 / D302			CE ABC A106	LIBRARY HOURS		CE BATCH 1 (ABC) PL-A107				
THU	EMIOT/ RTES/ I&HPR VAK / AVN / SAK BATCH 4 A202 / A109 / D302			EMIOT / RTES / I&HPR VAK / AVN / RPB A105/A106/A112	BE / AI / SSP UYV / RPB / SAP A105/A106/A112		CE BATCH 2 (ABC) PL-A107			DEPARTMENT MEETING	
FRI	PROJECT PRR A112	PROJECT PRR A112		PROJECT			PROJECT			PROJECT	

Consumer Electronics (CE) AMAR B. CHAVAN (ABC)  
Engineering Economics ANUSHI TIWARI (AT)  
Project MRUNALINI CHAVAN (MHC)

OPEN ELECTIVE (OE)  
  
DEPARTMENTAL  
ELECTIVE (DE)

Real Time Embedded System (RTES)  
Energy Management for IoT (EMIOT)  
Intelligent & High Performance Robotics (I&HPR)  
Biomedical Engineering (BE)  
Artificial Intelligence (AI)  
Speech Signal Processing (SSP)

AMIT NAGARALE (AVN)  
VAISHALI A. KATKAR (VAK)  
DR. RUSHIKESH P. BORSE (RPB)  
SHRIDHAR KHANDEKAR (SAK)  
USHA VERMA (UVV)  
DR. RUSHIKESH P. BORSE (RPB)  
SATYAJIT PANGAONKAR (SAP)

EDC ELECTRONICS DEVICE LAB  
ESL EMBEDDED SYSTEM LAB  
CL COMMUNICATION LAB  
ACL ADVANCED COMM. LAB  
RL ROBOTICS LAB  
PL PROJECT LAB

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